TPAZ8858-01F

1-Line Bi-directional ESD DIODE

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Features

- Low operating voltage: 60V
- 660W peak pulse power (8/20μs)
- Ultra low leakage: nA level
- Low clamping voltage
- –IEC 61000-4-2 (ESD) immunity test
  Air discharge: ±30kV
  Contact discharge: ±30kV
  –IEC61000-4-4 (EFT) 40A (5/50ns)
  –IEC61000-4-5 (Lightning) 6A (8/20μs)
- 2-pin leadless package
- These are Pb−Free Devices
- Response Time is Typically < 1 ns

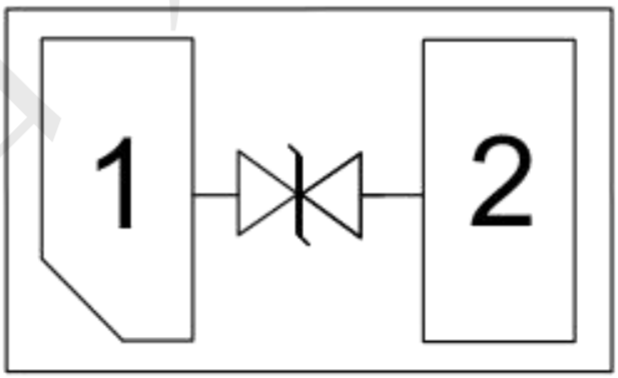
Mechanical Characteristics

- Package: DFN1610-2
- Lead Finish: Matte Tin
- Case Material: “Green” Molding Compound
- Terminal Connections: See Diagram Below
- -IEC 61000-4-2 (ESD) immunity test

Applications

- Power Management
- Power Line Protection
- Control Signal Line Protection
- Chip-On-Glass (COG) Panels
- Monitors and Flat Panel Displays

Dimensions and Pin Configuration



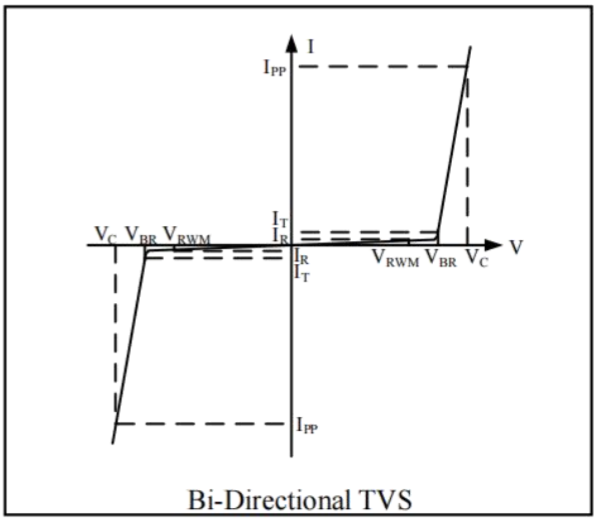
Absolute Maximum Ratings(Tamb=25°C unless otherwise specified)

Parameter	Symbol	Value	Unit
Peak Pulse Power (8/20μs)	Ppk	660	W
Peak Pulse Current (8/20μs)	Ipp	6	A
ESD per IEC 61000−4−2 (Air) ESD per IEC 61000−4−2 (Contact)	VESD	±30 ±30	KV
Operating Temperature Range	TJ	−55 to +125	°C
Storage Temperature Range	Tstg	−55 to +150	°C

Electrical Characteristics (TA=25°C unless otherwise specified)

Parameter	Symbol	Min	Typ	Max	Unit	Test Condition
Reverse Working Voltage	VRWM	--	--	60	V	
Breakdown Voltage	VBR	65	--	--	V	IT= 1mA
Reverse Leakage Current	IR	--	--	0.5	uA	VRWM=60V
Clamping Voltage	VC	--	--	85	V	Ipp=1A(8x 20us pulse)
Clamping Voltage	VC	--	100	110	V	Ipp=6A(8x 20us pulse)
Junction Capacitance	CJ	--	--	25	pF	VR = 0V, f = 1MHz

Symbol	Parameter
V_{RWM}	Nominal Reverse Working Voltage
I_R	Reverse Leakage Current @ V_{RWM}
V_{BR}	Reverse Breakdown Voltage @ I_T
I_T	Test Current for Reverse Breakdown
V_C	Clamping Voltage @ I_{PP}
I_{PP}	Maximum Peak Pulse Current
C_{ESD}	Parasitic Capacitance
V_R	Reverse Voltage
f	Small Signal Frequency



Bi-Directional TVS

Typical Performance Characteristics (TA=25°C unless otherwise Specified)

Fig1. 8/20μs Pulse Waveform

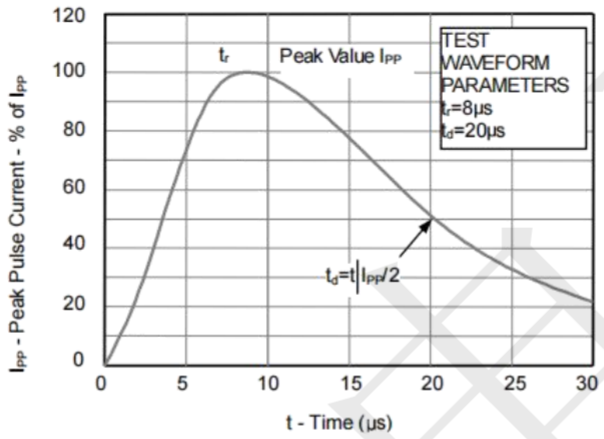


Fig2. ESD Pulse Waveform (according to IEC 61000-4-2)

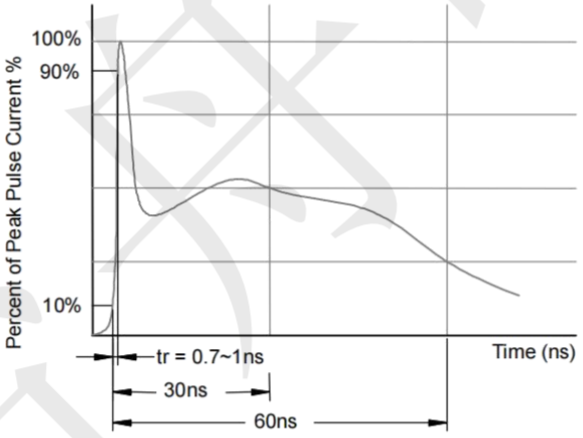
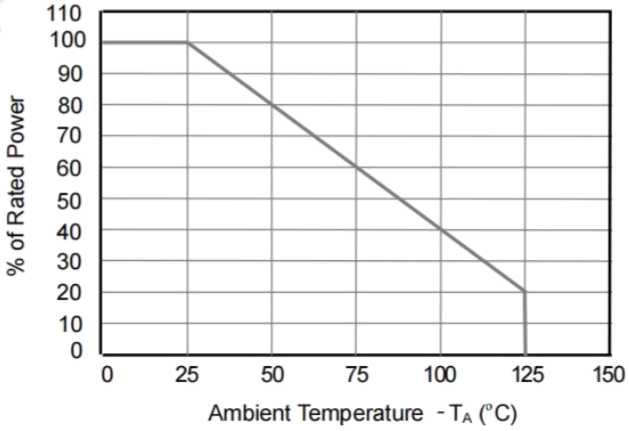
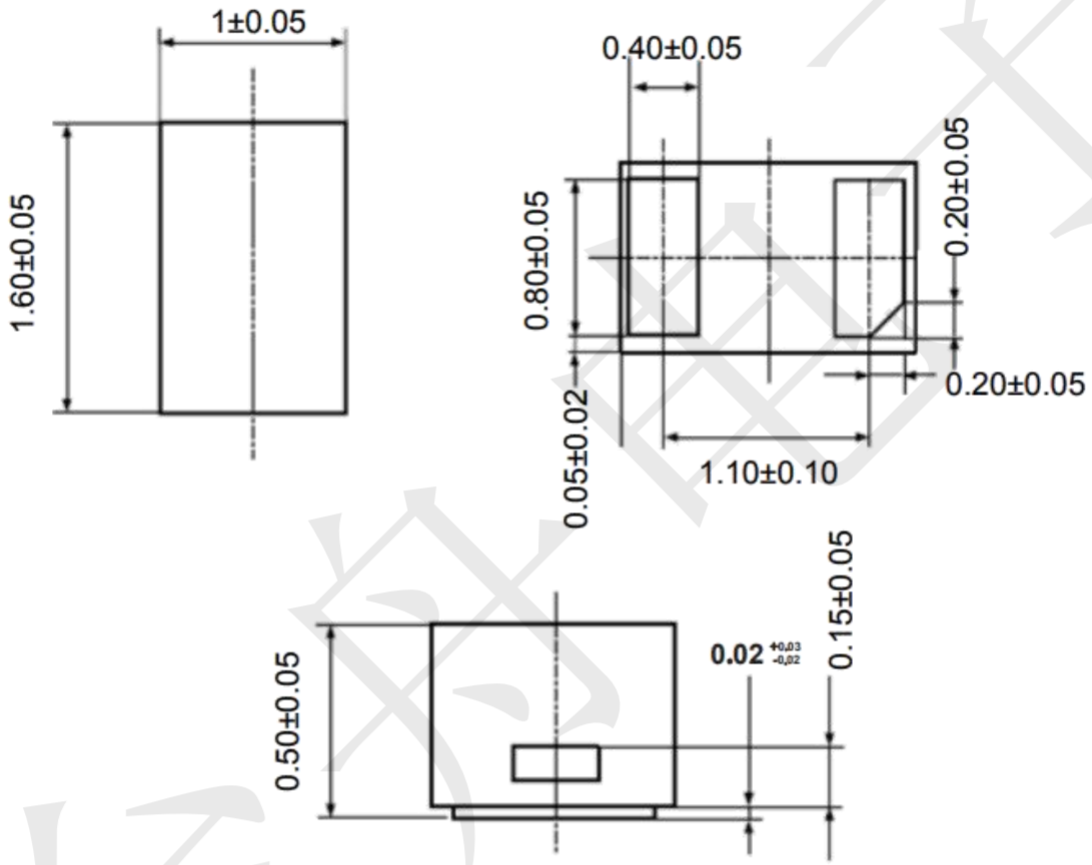


Fig3. Power Derating Curve



Package Outline Dimensions (unit: mm)

DFN1610-2



Mounting Pad Layout (unit: mm)

