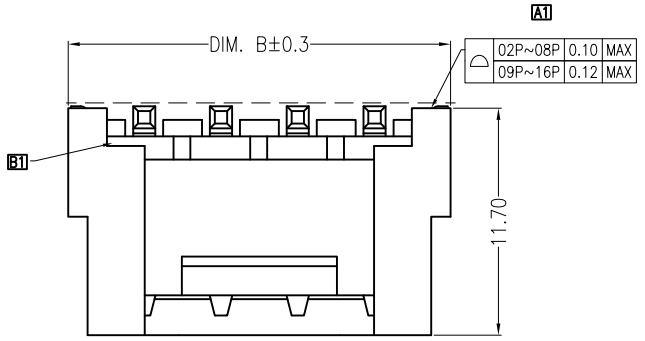
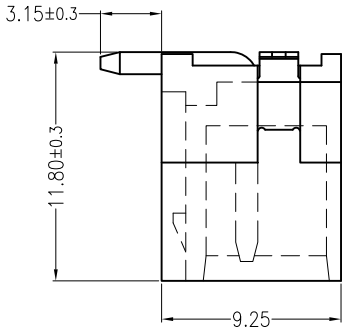
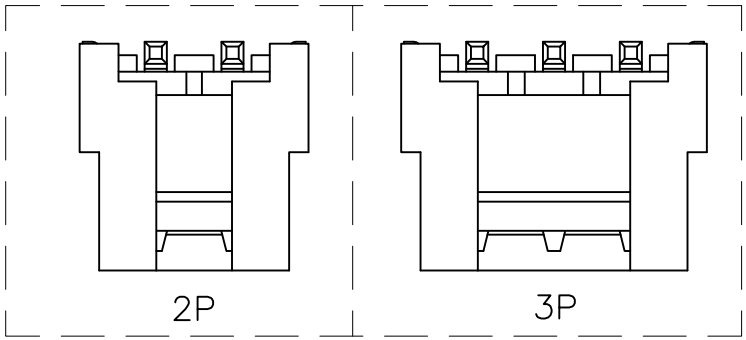
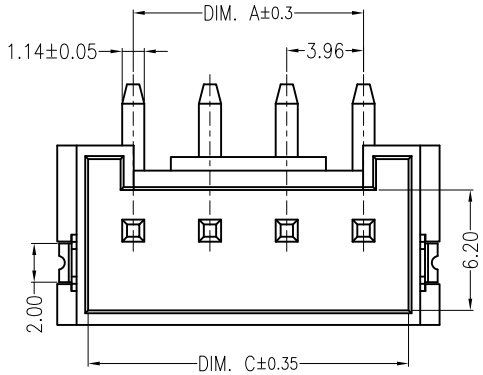
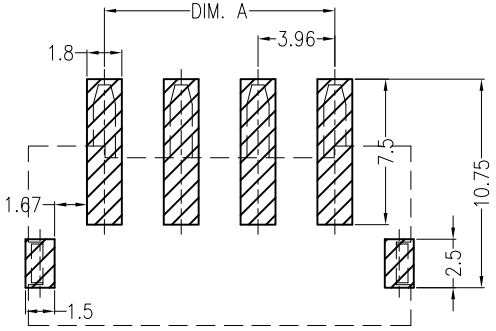
REV	DESCRIPTION	DESIGN	DATE
A1	Adjust flatness	Jiangjianyin	2020/06/18
B	UPGRADE VERSION	S. Tang	2024/02/23









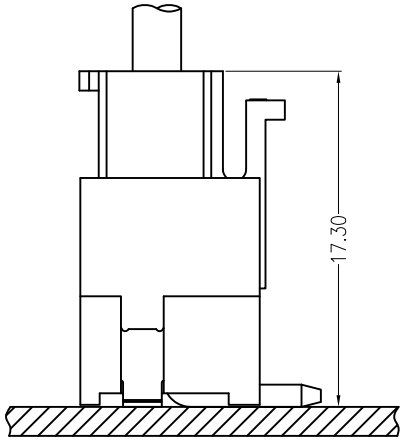


SUGGESTED PCB LAYOUT

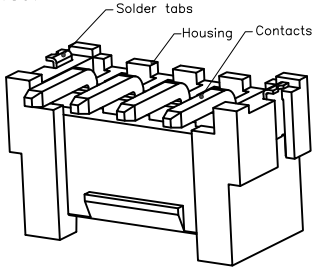
Specifications:

Contact resistance:≤20mΩ
Insulation resistance:≥1000MΩ
Rated voltage:250V AC DC
Rated current:7.0A AC DC
Withstand voltage:1500V AC/minute
Temperature range:−25℃～ +105℃
Housing:PA9T,UL94V−0, Natural
Contacts:Brass/Sn
Solder tabs:PhosphorBronze/Sn

Circuits	Dimensions(mm)		
	DIM. A	DIM. B	DIM. C
02	3.96	11.88	8.60
03	7.92	15.84	12.56
04	11.88	19.80	16.52
05	15.84	23.76	20.48
06	19.80	27.72	24.44
07	23.76	31.68	28.40
08	27.72	35.64	32.36
09	31.68	39.60	36.32
10	35.64	43.56	40.28
*11	39.60	47.52	44.24
*12	43.56	51.48	48.20
*13	47.52	55.44	52.16
*14	51.48	59.40	56.12
*15	55.44	63.36	60.08
*16	59.40	67.32	64.04



Assembly Layout



3D VIEWS

ORDER INFORMATION:

A3962AWV−**−D1CA2−*

- PART No.
- No.OF CIRCUITS: 02~10
- PLASTIC MATERIAL: D1C=PA9T(NATURAL)
- PLATING: A=SN
- METAL CODE: 2=BRASS
- PACKAGING: R=REEL P=PE G=TUBE

LXW 连兴旺电子(深圳)有限公司 LXWCONN ELECTRONICS(SHENZHEN)CO.,LTD			TITLE: 3.96mmPITCH 180°WAFER SMT TYPE
GENERAL TOLERANCE		USE: CUSTOMER	PART NO.: A3962AWV−**−D1CA2−*
X.X ±0.30	X. ±5°	APPD: J. J. Yao	DWG NO.: GCKH−0577
X.XX ±0.20	X.X ±2°	CHKD: P. Yan	SCALE 1 : 1 / SHEET 1 / 1
X.XXX ±0.10	X.XX ±1°	DR: S. Tang	
	UNITS: mm		