# STL12N10F7-VB Datasheet
# N-Channel 100V (D-S) MOSFET

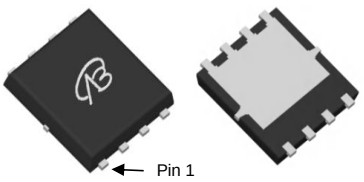
| PRODUCT SUMMARY | | |
|---|---|---|
| $V_{DS}$ (V) | $R_{DS(on)}$ (Ω) | $I_D$ (A)[a] |
| 100 | 0.011 at $V_{GS}$ = 10 V | 50 |
| | 0.014 at $V_{GS}$ = 4.5 V | 45 |

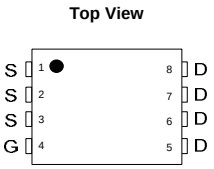
## FEATURES

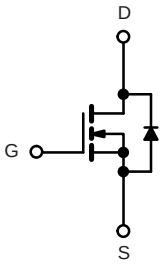
- 175 °C Junction Temperature
- SGT technology Power MOSFET
- Material categorization:

RoHS COMPLIANT

DFN 3x3




N-Channel MOSFET

| ABSOLUTE MAXIMUM RATINGS ($T_C$ = 25 °C, unless otherwise noted) | | | | |
|---|---|---|---|---|
| **Parameter** | | **Symbol** | **Limit** | **Unit** |
| Gate-Source Voltage | | $V_{GS}$ | ±20 | V |
| Continuous Drain Current ($T_J$ = 175 °C)[b] | $T_C$ = 25 °C | $I_D$ | 50 | A |
| | $T_C$ = 100 °C | | 30[a] | |
| Pulsed Drain Current | | $I_{DM}$ | 150 | |
| Continuous Source Current (Diode Conduction) | | $I_S$ | 85[a] | |
| Avalanche Current | | $I_{AS}$ | 115 | |
| Single Avalanche Energy (Duty Cycle ≤ 1 %) | L = 0.1 mH | $E_{AS}$ | 130 | mJ |
| Maximum Power Dissipation | $T_C$ = 25 °C | $P_D$ | 136 | W |
| | $T_A$ = 25 °C | | 3[b], 8.3[b, c] | |
| Operating Junction and Storage Temperature Range | | $T_J$, $T_{stg}$ | - 55 to 175 | °C |

| THERMAL RESISTANCE RATINGS | | | | | |
|---|---|---|---|---|---|
| **Parameter** | | **Symbol** | **Typical** | **Maximum** | **Unit** |
| Maximum Junction-to-Ambient[a] | t ≤ 10 sec | $R_{thJA}$ | 15 | 18 | °C/W |
| | Steady State | | 40 | 50 | |
| Maximum Junction-to-Case | | $R_{thJC}$ | 0.85 | 1.1 | |

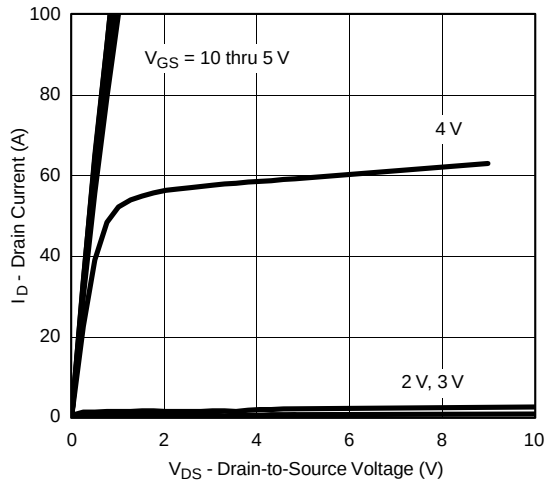
Notes:
a. Package limited.
b. Surface mounted on 1" x 1" FR4 board.
c. t ≤ 10 s.

| **SPECIFICATIONS** ($T_J$ = 25 °C, unless otherwise noted) | | | | | | |
|---|---|---|---|---|---|---|
| **Parameter** | **Symbol** | **Test Conditions** | **Min.** | **Typ.[a]** | **Max.** | **Unit** |
| **Static** | | | | | | |
| Drain-Source Breakdown Voltage | $V_{DS}$ | $V_{GS}$ = 0 V, $I_D$ = 250 µA | 100 | | | V |
| Gate Threshold Voltage | $V_{GS(th)}$ | $V_{DS}$ = $V_{GS}$, $I_D$ = 250 µA | 1 | 2 | 3 | |
| Gate-Body Leakage | $I_{GSS}$ | $V_{DS}$ = 0 V, $V_{GS}$ = ± 20 V | | | ± 100 | nA |
| Zero Gate Voltage Drain Current | $I_{DSS}$ | $V_{DS}$ = 100V, $V_{GS}$ = 0 V | | | 1 | µA |
| | | $V_{DS}$ = 100V, $V_{GS}$ = 0 V, $T_J$ = 125 °C | | | 50 | |
| | | $V_{DS}$ = 100V, $V_{GS}$ = 0 V, $T_J$ = 175 °C | | | 250 | |
| On-State Drain Current[b] | $I_{D(on)}$ | $V_{DS}$ = 5 V, $V_{GS}$ = 10 V | 60 | | | A |
| Drain-Source On-State Resistance[b] | $R_{DS(on)}$ | $V_{GS}$ = 10 V, $I_D$ = 20 A | | 0.011 | | Ω |
| | | $V_{GS}$ = 10 V, $I_D$ = 20 A, $T_J$ = 125 °C | | 0.016 | | |
| | | $V_{GS}$ = 10 V, $I_D$ = 20 A, $T_J$ = 175 °C | | 0.022 | | |
| | | $V_{GS}$ = 4.5 V, $I_D$ = 20A | | 0.014 | | |
| Forward Transconductance[b] | $g_{fs}$ | $V_{DS}$ = 15 V, $I_D$ = 20 A | | 60 | | S |
| **Dynamic** | | | | | | |
| Input Capacitance | $C_{iss}$ | $V_{GS}$ = 0 V, $V_{DS}$ = 25 V, f = 1 MHz | | 5000 | | pF |
| Output Capacitance | $C_{oss}$ | | | 470 | | |
| Reverse Transfer Capacitance | $C_{rss}$ | | | 225 | | |
| Total Gate Charge[c] | $Q_g$ | $V_{DS}$ = 30 V, $V_{GS}$ = 10 V, $I_D$ = 50 A | | 70 | 70 | nC |
| Gate-Source Charge[c] | $Q_{gs}$ | | | 11 | | |
| Gate-Drain Charge[c] | $Q_{gd}$ | | | 13 | | |
| Turn-On Delay Time[c] | $t_{d(on)}$ | $V_{DD}$ = 30 V, $R_L$ = 0.6 Ω<br>$I_D$ ≅ 50 A, $V_{GEN}$ = 10 V, $R_g$ = 2.5 Ω | | 19 | 25 | ns |
| Rise Time[c] | $t_r$ | | | 16 | 23 | |
| Turn-Off Delay Time[c] | $t_{d(off)}$ | | | 36 | 56 | |
| Fall Time[c] | $t_f$ | | | 25 | 35 | |
| **Source-Drain Diode Ratings and Characteristics** ($T_C$ = 25 °C) | | | | | | |
| Pulsed Current | $I_{SM}$ | | | | 150 | A |
| Diode Forward Voltage | $V_{SD}$ | $I_F$ = 20 A, $V_{GS}$ = 0 V | | 1 | 1.5 | V |
| Reverse Recovery Time | $t_{rr}$ | $I_F$ = 20 A, di/dt = 100 A/µs | | 4 | 135 | ns |

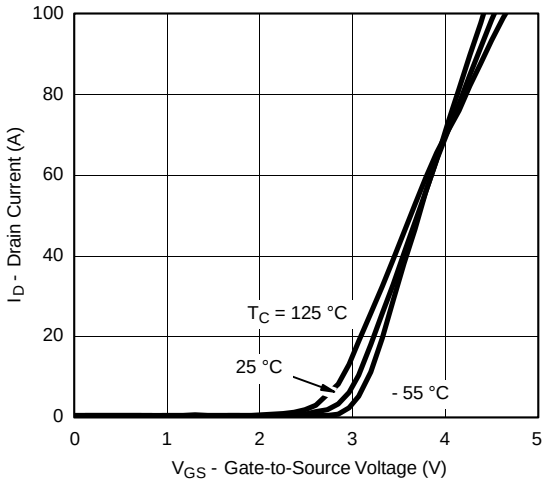
Notes:
a. For design aid only; not subject to production testing.
b. Pulse test; pulse width ≤ 300 µs, duty cycle ≤ 2 %.
c. Independent of operating temperature.

*Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.*

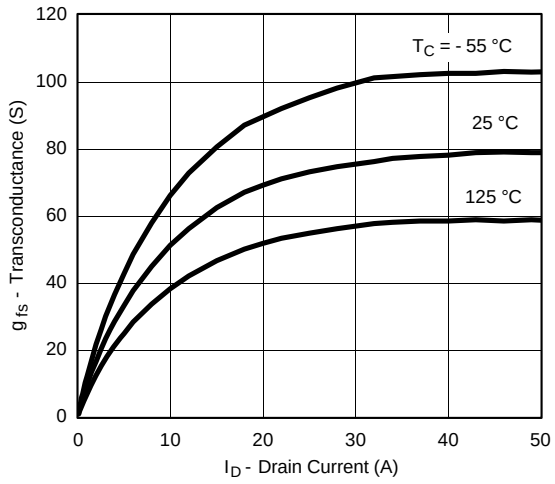
## TYPICAL CHARACTERISTICS (25 °C unless noted)



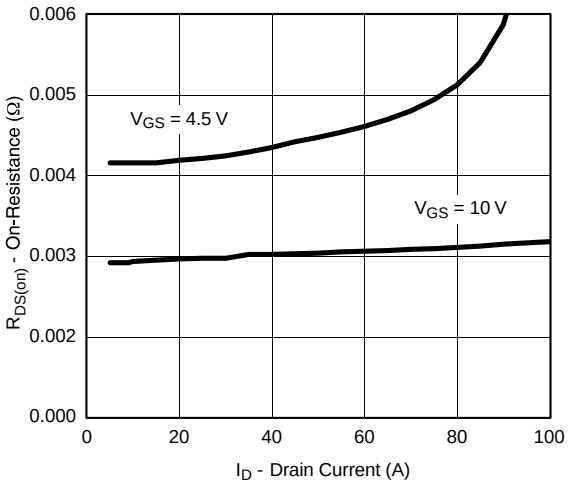

**Output Characteristics**



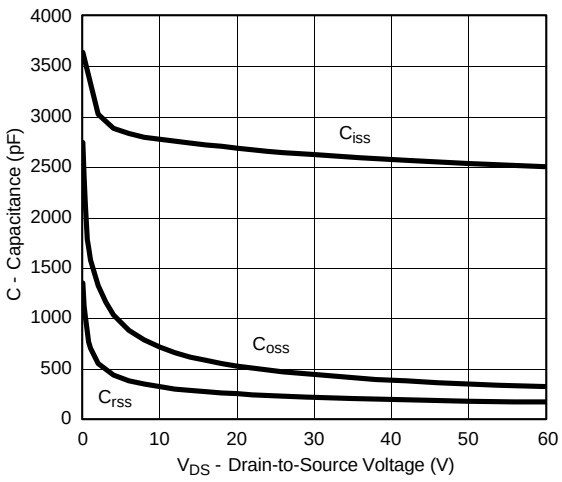

**Transfer Characteristics**



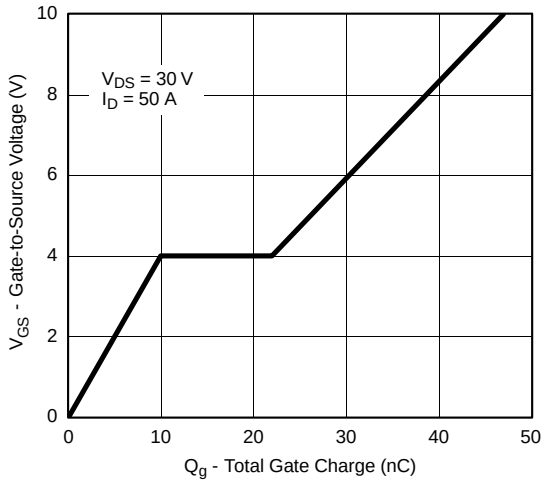

**Transconductance**




**On-Resistance vs. Drain Current**

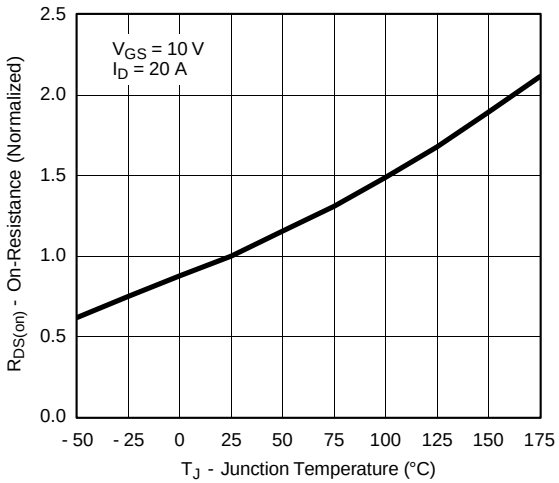



**Capacitance**

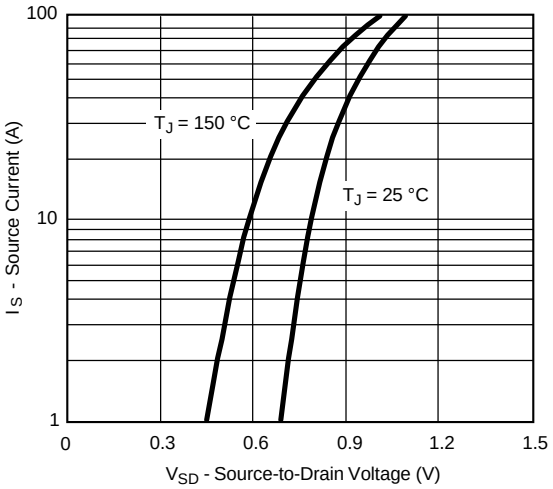



**Gate Charge**

## TYPICAL CHARACTERISTICS (25 °C unless noted)

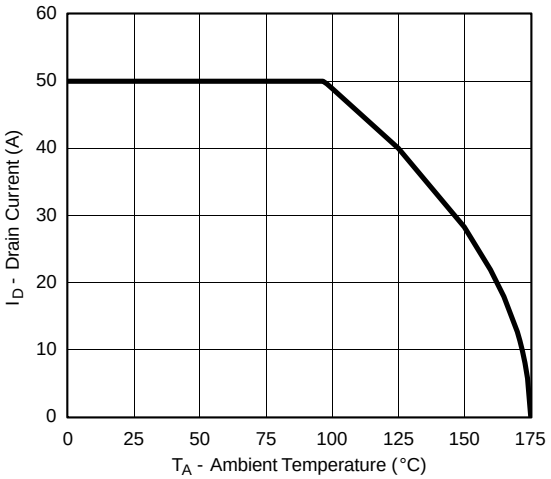



**On-Resistance vs. Junction Temperature**

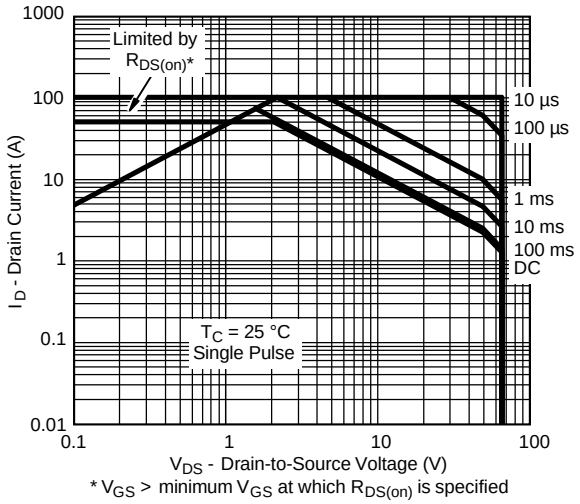



**Source-Drain Diode Forward Voltage**

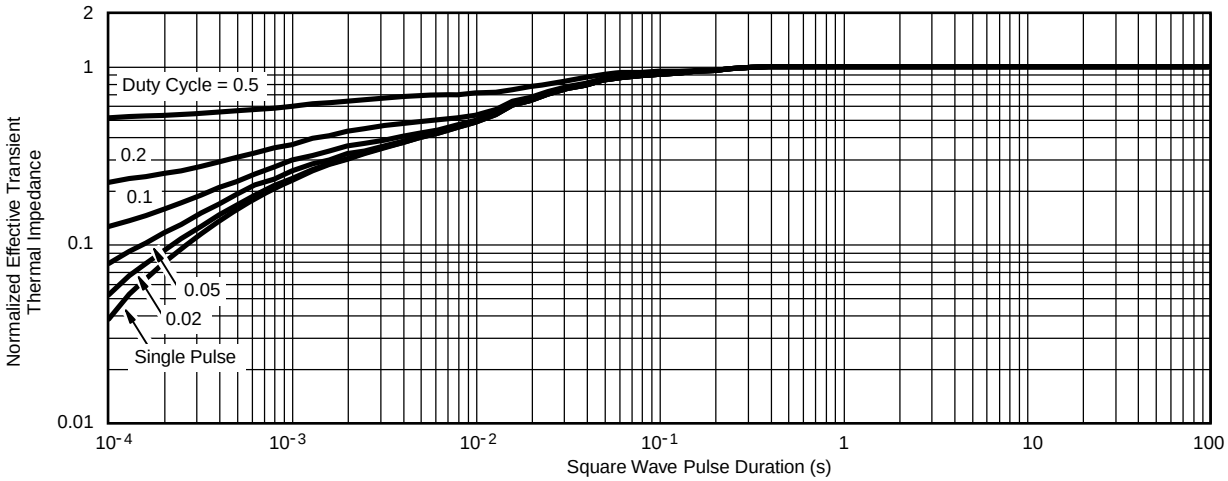
## THERMAL RATINGS




**Maximum Drain Current vs. Ambient Temperature**

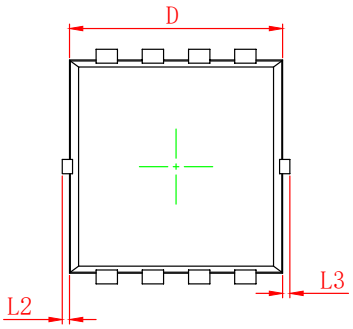



**Safe Operating Area**

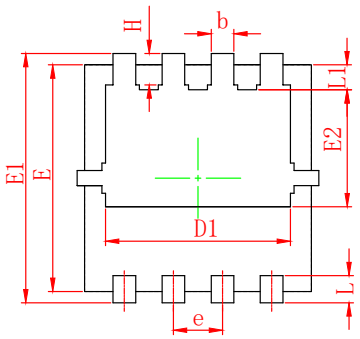



**Normalized Thermal Transient Impedance, Junction-to-Case**

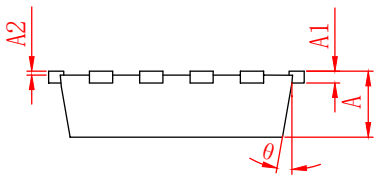
## **PDFNWB3×3-8L** Package Outline Dimensions




Top View
[顶视图]



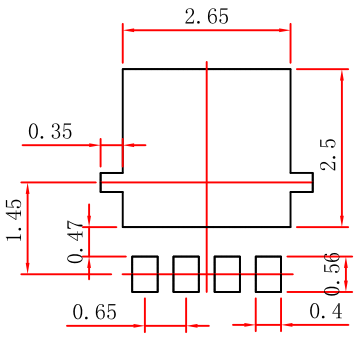

Bottom View
[背视图]




Side View
[侧视图]

| Symbol | Dimensions In Millimeters | | Dimensions In Inches | |
|---|---|---|---|---|
| | Min | Max | Min | Max |
| A | 0.650 | 0.850 | 0.026 | 0.033 |
| A1 | 0.203REF. | | 0.008REF. | |
| A2 | 0~0.05 | | 0~0.002 | |
| D | 2.900 | 3.100 | 0.114 | 0.122 |
| D1 | 2.050 | 2.550 | 0.081 | 0.100 |
| E | 2.900 | 3.100 | 0.114 | 0.122 |
| E1 | 3.150 | 3.450 | 0.124 | 0.136 |
| E2 | 1.450 | 1.650 | 0.057 | 0.065 |
| b | 0.200 | 0.400 | 0.008 | 0.016 |
| e | 0.550 | 0.750 | 0.022 | 0.030 |
| L | 0.300 | 0.500 | 0.012 | 0.020 |
| L1 | 0.180 | 0.480 | 0.007 | 0.019 |
| L2 | 0~0.100 | | 0~0.004 | |
| L3 | 0~0.100 | | 0~0.004 | |
| H | 0.315 | 0.515 | 0.012 | 0.020 |
| Φ | 9° | 13° | 9° | 13° |

## Suggested Pad Layout




Note:
1.Controlling dimension:in millimeters.
2.General tolerance:±0.05mm.
3.The pad layout is for reference purposes only.

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