DFN0603-2L Plastic-Encapsulate ESD Protection Diodes

Features

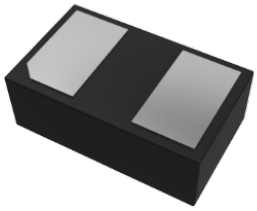
- Low leakage current
- DFN0603-2L surface mount package
- IEC 61000-4-2 (ESD Air): ±20kV
- IEC 61000-4-2 (ESD Contact): ±18kV
- IEC 61000-4-5 (Lightning 8/20μs): 6A

Reverse Working Voltage
3.3V Max.
Ultra Small Capacitance
0.25pF(Typ.)

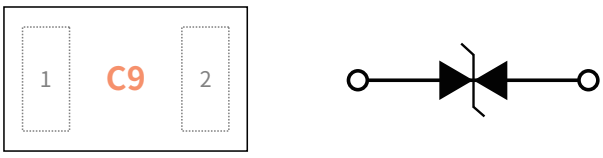
Applications

- Cellular Handsets and Accessories
- Personal Digital Assistants
- Notebooks and Handhelds
- Portable Instrumentation,Digital Cameras
- Peripherals, Audio Players, Industrial Equipment

DFN0603-2L



Function Diagram

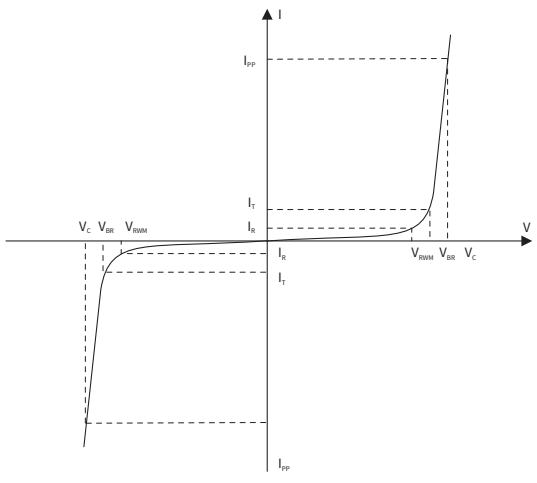


Maximum Ratings (Ta=25°C Unless otherwise specified)

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
V_{ESD}	Electrostatic Discharge Voltage	ESD per IEC 61000-4-2(Air)	±20	KV
		ESD per IEC 61000-4-2(Contact)	±18	KV
P_{PP}	Peak Pulse Power	tp = 8/20 μs	42	W
I_{PP}	Rated Peak Pulse Current	tp = 8/20 μs	6	A
T_J	Operating JunctionTemperature Range	—	-40 to +125	°C
T_{STG}	Storage Temperature Range	—	-55 to +150	°C

Electrical Parameter

SYMBOL	PARAMETER
I_{PP}	Peak Pulse Current @8/20μs
V_C	Clamping Voltage @ I_{PP}
I_{BR} / I_T	Test Current
V_{BR}	Breakdown Voltage @ I_T
V_{RWM}	Peak Reverse Working Voltage
I_R	Reverse Leakage Current @ V_{RWM}
I_{TRIG}	Reverse Trigger Current
V_{TRIG}	Reverse Trigger Voltage
I_{HOLD}	Reverse Holding Current
V_{HOLD}	Reverse Holding Voltage
P_{PP}	Peak Pulse Power Dissipation



Electrical Characteristics (Ta=25°C Unless otherwise specified)

PARAMETER	SYMBOL	CONDITION	Min	Typ	Max	UNIT
Peak Reverse Working Voltage	V_{RWM}	T_a=25°C	—	—	3.3	V
Breakdown Voltage	V_{BR}	I_R=1mA,T_a=25°C	5.0	—	—	V
Reverse Leakage Current	I_R	V_R=3.3V,T_a=25°C	—	—	0.1	μA
Reverse Holding Voltage	V_{HOLD}	I_{HOLD}=50mA,T_a=25°C	2.0	—	—	V
Dynamic Resistance	R_{DYN}	Z0=50Ω，calculated from 4A to 16A	—	0.27	—	Ω
TLP Clamping Voltage	V_C@TLP	Z0=50Ω,Tp=100ns,Tr=1ns;TLP@16A	—	8.5	—	V
Clamping Voltage	V_C	I_{PP}=6A,t_p=8/20μs	—	7.0	—	V
Junction Capacitance	C_J	V_R=0V,f=1MHz	—	0.25	0.35	pF

Ratings And Characteristics Curves (Ta=25°C Unless otherwise specified)

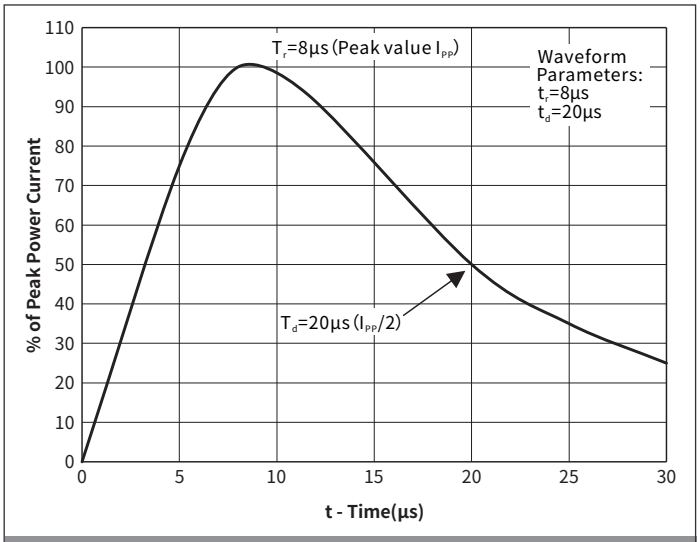


Fig.1 Pulse Waveform

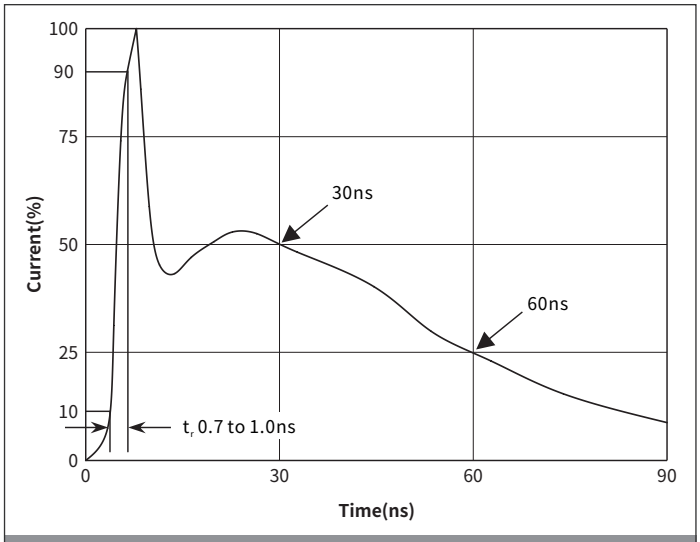


Fig.2 Pulse Waveform-ESD(IEC61000-4-2)

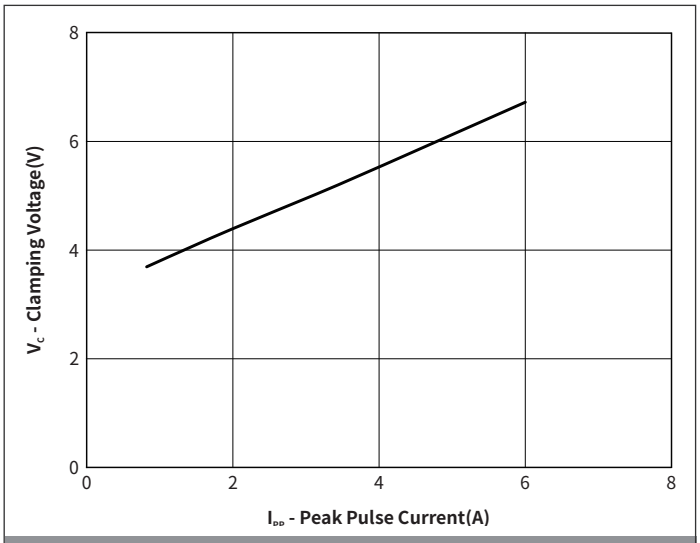


Fig.3 Clamping Voltage vs. Peak Pulse Current

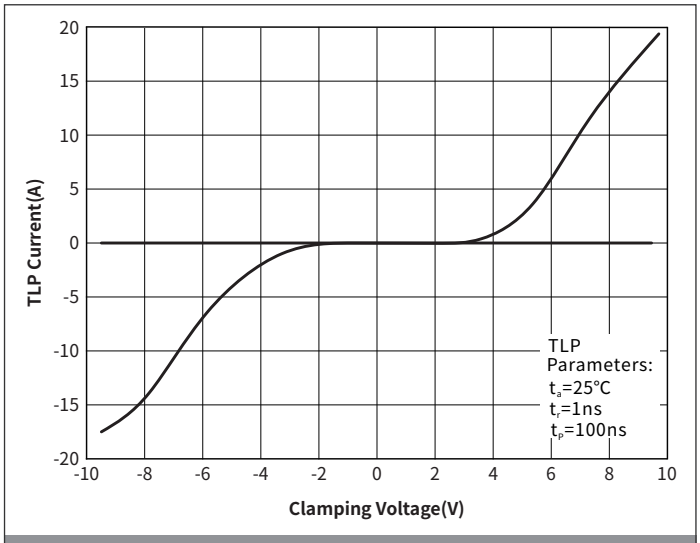
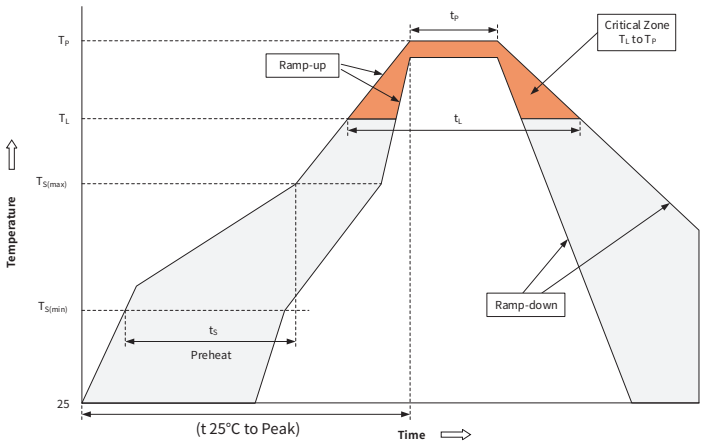


Fig.4 TLP IV Curve

Ordering Information

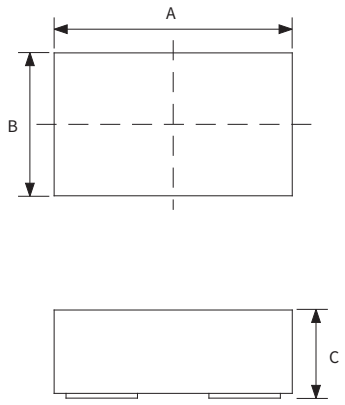
PREFERED P/N	PACKAGE	SIZE(mm)	DELIVERY MODE	MPQ(PCS)
H3V3U06BS	DFN0603-2L	0.6×0.30×0.30	7''REEL	10,000

Recommended Soldering Conditions



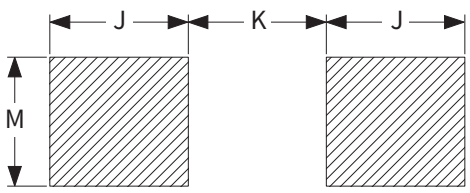
Profile Feature		Pb-Free Assembly
Pre-heat	Temperature Min ($T_{S(min)}$)	+150°C
	Temperature Max($T_{S(max)}$)	+200°C
	Time (Min to Max) (t_S)	60-180 secs.
Average ramp up rate (Liquid us Temp (T_L) to peak)		3°C /sec. Max
$T_{S(max)}$ to T_L - Ramp-up Rate		3°C /sec. Max
Reflow	Temperature(T_L)(Liquid us)	+217°C
	Temperature(t_L)	60-150 secs.
Peak Temp (T_P)		+260(+0/-5)°C
Time within 5°C of actual Peak Temp (t_P)		20-40secs
Ramp-down Rate		6°C /sec. Max
Time 25°C to Peak Temp (T_P)		8 min. Max
Do not exceed		+260°C

Package Outline Dimensions (DFN0603-2L)



Symbol	Dimensions			
	Millimeters		Inches	
	Min.	Max.	Min.	Max.
A	0.55	0.65	0.022	0.026
B	0.25	0.35	0.010	0.014
C	0.23	0.34	0.009	0.013
D	0.35	0.45	0.014	0.018
D_1	0.16	0.22	0.006	0.009
E	0.20	0.30	0.008	0.012

Suggested Pad Layout



Symbol	Dimensions			
	Millimeters		Inches	
	Min.	Max.	Min.	Max.
J	0.20	0.24	0.008	0.009
K	0.18	0.22	0.007	0.009
M	0.30	0.34	0.012	0.013