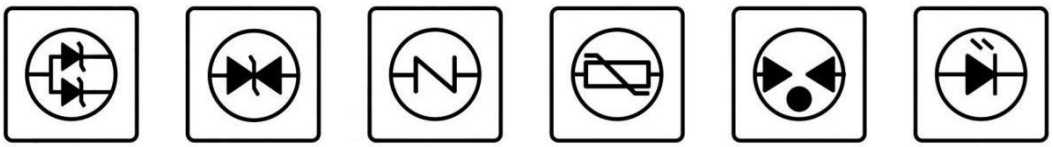
MSKSEMI 美森科
SEMICONDUCTOR



PMEG6020ER-MS

Product specification

PMEG6020ER-MS

Surface Mount Schottky Barrier Rectifier
Reverse Voltage - 60 V
Forward Current - 2.0A

FEATURES

- Metal silicon junction, majority carrier conduction
- For surface mounted applications
- Low power loss, high efficiency
- High forward surge current capability
- For use in low voltage, high frequency inverters, free wheeling, and polarity protection applications

Reference News

PACKAGE OUTLINE	CIRCUIT DIAGRAM	MARKING
		

MECHANICAL DATA

- Case: SOD-123FL
- Terminals: Solderable per MIL-STD-750, Method 2026
- Approx. Weight:15mg 0.00048oz

PINNING

PIN	DESCRIPTION
1	Cathode
2	Anode

Absolute Maximum Ratings and Electrical characteristics

Ratings at 25℃ ambient temperature unless otherwise specified. Single phase, half wave, 60Hz resistive or inductive load, for capacitive load, derate by 20 %

Parameter	Symbols	PMEG6020ER-MS	Units
Maximum Repetitive Peak Reverse Voltage	V_{RRM}	60	V
Maximum RMS voltage	V_{RMS}	42	V
Maximum DC Blocking Voltage	V_{DC}	60	V
Maximum Average Forward Rectified Current	$I_{F(AV)}$	2.0	A
Peak Forward Surge Current ,8.3ms Single Half Sine-wave Superimposed on Rated Load (JEDEC method)	I_{FSM}	50	A
Max Instantaneous Forward Voltage at 2A	V_F	0.70	V
Maximum DC Reverse Current at Rated DC Reverse Voltage (T_a = 25℃ / T_a = 100℃)	I_R	0.5 / 5 | 0.3 / 3	mA
Typical Junction Capacitance (1)	C_j	220 | 80	pF
Typical Thermal Resistance (2)	$R_{\theta JA}$	85	℃/W
Operating Junction Temperature Range	T_j	-55 ~ +150	℃
Storage Temperature Range	T_{stg}	-55 ~ +150	℃

(1) Measured at 1 MHz and applied reverse voltage of 4 V D.C

(2) P.C.B. mounted with 2.0" X 2.0" (5 X 5 cm) copper pad areas.

Copyright© Msksemi Incorporated

Fig.1 Forward Current Derating Curve

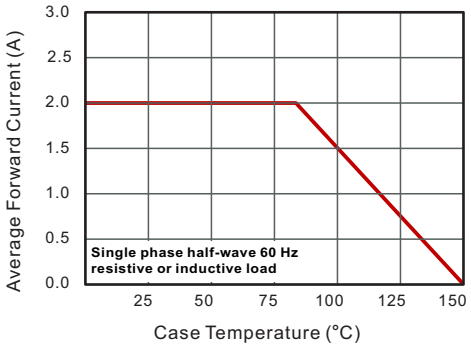


Fig.2 Typical Reverse Characteristics

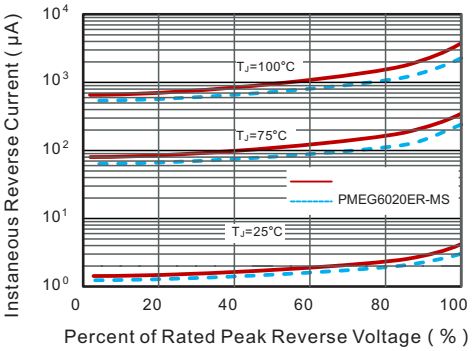


Fig.3 Typical Forward Characteristic

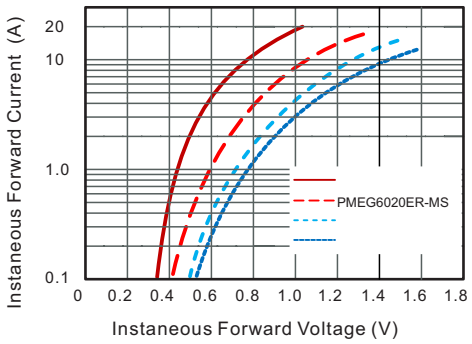


Fig.4 Typical Junction Capacitance

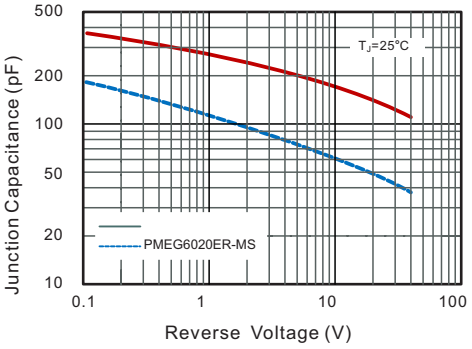


Fig.5 Maximum Non-Repetitive Peak Forward Surage Current

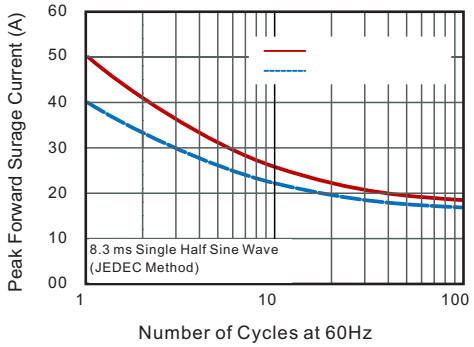
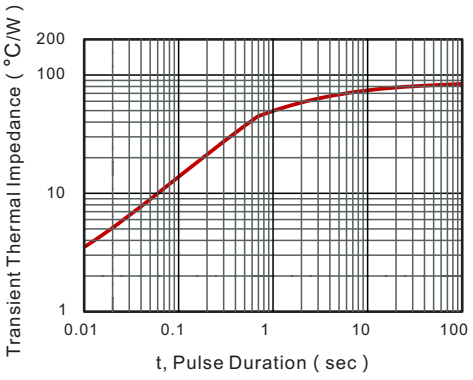
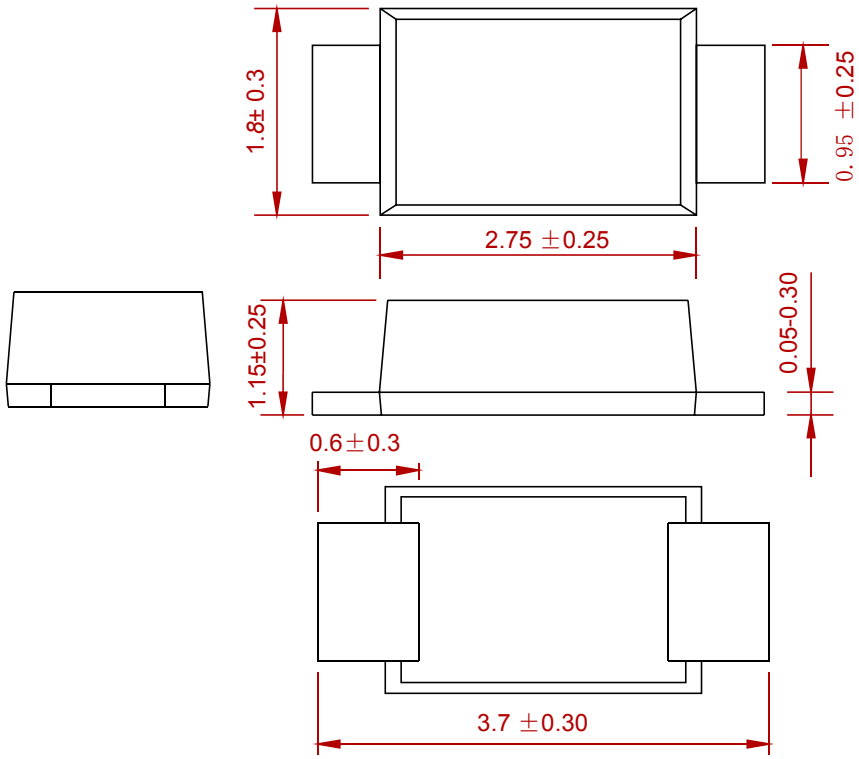


Fig.6- Typical Transient Thermal Impedance



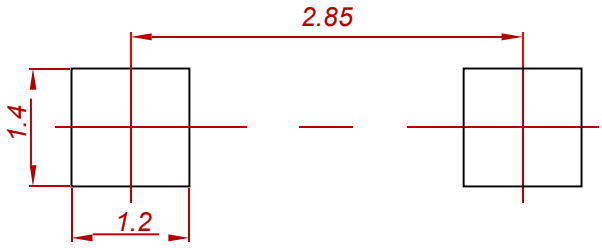
Copyright© Msksemi Incorporated

PACKAGE MECHANICAL DATA



Dimensions in millimeters

Suggested Pad Layout



Note:
1.Controlling dimension:in millimeters.
2.General tolerance:±0.05mm.
3.The pad layout is for reference purposes only.

REEL SPECIFICATION

P/N	PKG	QTY
PMEG6020ER-MS	SOD-123FL	3000

Copyright© Msksemi Incorporated

Attention

■ Any and all MSKSEMI Semiconductor products described or contained herein do not have specifications that can handle applications that require extremely high levels of reliability, such as life-support systems, aircraft's control systems, or other applications whose failure can be reasonably expected to result in serious physical and/or material damage. Consult with your MSKSEMI Semiconductor representative nearest you before using any MSKSEMI Semiconductor products described or contained herein in such applications.

■ MSKSEMI Semiconductor assumes no responsibility for equipment failures that result from using products at values that exceed, even momentarily, rated values (such as maximum ratings, operating condition ranges, or other parameters) listed in products specificationsof any andall MSKSEMI Semiconductor products described orcontained herein.

■ Specifications of any and all MSKSEMI Semiconductor products described or contained herein stipulate the performance, characteristics, and functions of the described products in the independent state, and are not guarantees of the performance, characteristics, and functions of the described products as mounted in the customer's products or equipment. To verify symptoms and states that cannot be evaluated in an independent device, the customer should always evaluate and test devices mounted in the customer'sproducts orequipment.

■ MSKSEMI Semiconductor. strives to supply high-quality high-reliability products. However, any and all semiconductor products fail with someprobability. It is possiblethat these probabilistic failures could give rise to accidents or events that could endanger human lives, that could give rise to smoke or fire, or that could cause damage to other property. When designing equipment, adopt safety measures so that these kinds of accidents or events cannot occur. Such measures include but are not limited to protective circuits anderror prevention circuitsfor safedesign, redundant design, and structural design.

■ In the event that any or all MSKSEMI Semiconductor products(including technical data, services) described or contained herein are controlled under any of applicable local export control laws and regulations, such products must not be exported without obtaining the export license from theauthorities concerned in accordance with the above law.

■ No part of this publication may be reproduced or transmitted in any form or by any means, electronic or mechanical, including photocopying and recording, or any information storage or retrieval system, or otherwise, without the prior written permission of MSKSEMI Semiconductor.

■ Information (including circuit diagrams and circuit parameters) herein is for example only ; it is not guaranteed for volume production. MSKSEMI Semiconductor believes information herein is accurate and reliable, but no guarantees are made or implied regarding its use or any infringementsof intellectual property rights or other rightsof third parties.

■ Any and all information described or contained herein are subject to change without notice due to product/technology improvement, etc. Whendesigning equipment, referto the "Delivery Specification" for the MSKSEMI Semiconductor productthat you intend to use.

Copyright© Msksemi Incorporated