Features

- Low quiescent current : 0.8uA
- Wide input voltage range : 1.2V to 6.0V
- High output current : 500mA
- Low dropout voltage : 130mV at 100mA
- PSRR：65dB/1kHz
- Fixed output voltages： 1.2V, 1.5V, 1.8V, 2.5V, 2.8V, 3.0V, 3.3V, 3.6V.
- Output voltage tolerance : ±2%
- Current limit protection
- Short circuit protection
- Thermal shutdown protection
- Available packages：SOT23-5,SOT89-3,DFN1×1

Applications

- Battery-powered equipment
- Smoke detector and sensor
- Micro controller applications
- Home appliance

Description

The WL9002H series is an ultra-small, low dropout (LDO) linear regulator that can source 500mA of output current.The WL9002H has fast response to input voltage transient and load current transient, and ensures no overshoot voltage during WL9002H startup and short circuit recovery.

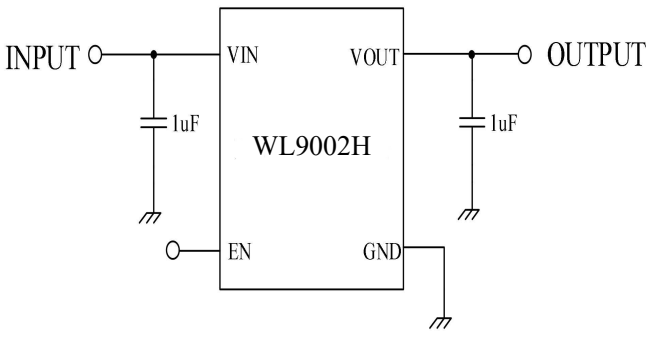
The WL9002H series has thermal shutdown, current limit, and short circuit protections for added safety.

The WL9002H series contains eight fixed output voltages of 1.2V, 1.5V, 1.8V,2.5V 2.8V, 3.0V, 3.3V,3.6V.

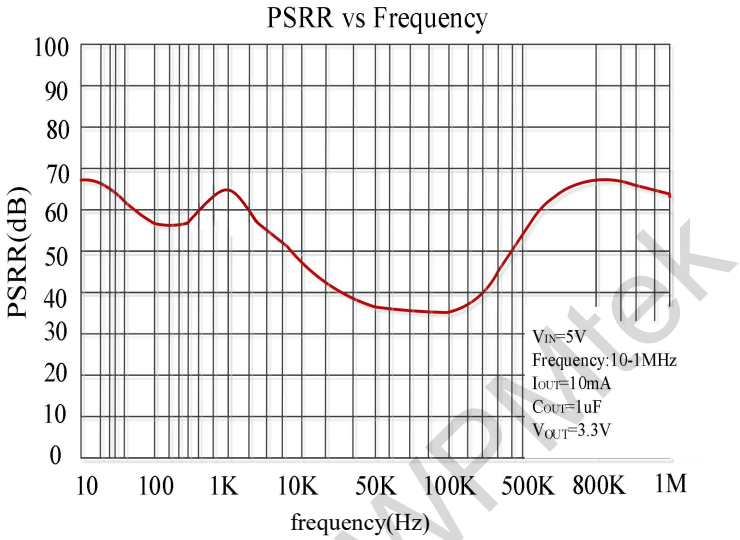
PART NUMBER	PACKAGE	BODY SIZE(NOM)
WL9002HS5	SOT23-5	2.9mm*2.8mm
WL9002HP3	SOT89-3	4.5mm*4.2mm
WL9002HD4	DFN1×1	1mm*1mm

(1) For all available packages, see the orderable addendum at the end of the data sheet.

Typical Application

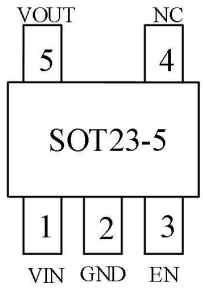


PSRR

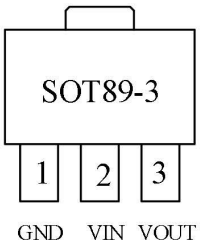


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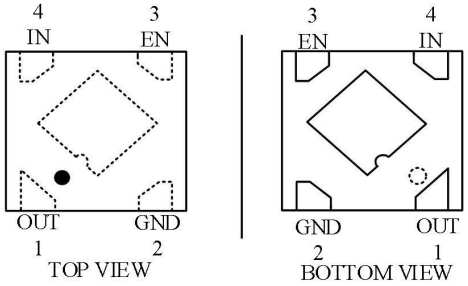
Pin Configuration and Functions



WL9002HS5



WL9002HP3



WL9002HD4

Pin Functions

Name	SOT23-5	SOT89-3	DFN1×1	Description
	WL9002HS5	WL9002HP3	WL9002HD4	
VIN	1	2	4	Input pin
GND	2	1	2	Ground pin
EN	3		3	Enable pin
NC	4			No connection
VOUT	5	3	1	Output pin

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Absolute Maximum Ratings

Parameter	Description	Min	Max	Unit
Input voltage	VIN to GND	-0.3	6	V
	VOUT to GND	-0.3	5	V
	VIN to VOUT	-0.3	5	V
	EN to GND	-0.3	6	V
Current	Peak output current	Internally limited		
Temperature	Operating temperature range	-40	125	℃
	Storage temperature	-40	150	℃
Thermal resistance (Junction to ambient)	SOT89-3	130		℃/W
	DFN1×1	300		
	SOT23-5	200		
Power dissipation	SOT89-3	900		mW
	DFN1×1	400		
	SOT23-5	600		

Note:

exceeding the range specified by the rated parameters will cause damage to the chip, and the working state of the chip beyond the range of rated parameters cannot be guaranteed. Exposure outside the rated parameter range will affect the reliability of the chip.

ESD Ratings

Parameter	Description	Range	Unit
V_{ESD}	Human body model(HBM)	4	KV
	Charged device model(CDM)	200	V

Note:

JEDEC document JEP155 states that 500-V HBM allows safe manufacturing with a standard ESD control process.

JEDEC document JEP157 states that 200-V CDM allows safe manufacturing with a standard ESD control process.

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Electrical Characteristics

(At T_A=25°C, C_{IN}=1uF, V_{IN}=V_{OUTNOM}+1V, C_{OUT}=1uF, unless otherwise noted)

Symbol	Parameter	Test Conditions	Min	Typ	Max	Unit
V_{IN}	Operating input voltage		1.2	—	6.0	V
I_{GND}	Quiescent current	V_{IN}=5V, No load	—	0.8	—	uA
V_{OUT}	Output voltage	V_{IN}=5V, I_{OUT}=10mA	V_{OUTNOM} * 0.98	V_{OUTNOM}	V_{OUTNOM} * 1.02	V
I_{OUT_MAX}	Output current		—	—	500	mA
V_{DROP}	Dropout voltage (WL9002H)	I_{OUT}=100mA , V_{IN}=V_{OUTNOM}-0.1V	—	130	—	mV
$\Delta V_{OUT}/\Delta I_{OUT}$	Load regulation	V_{IN}=4V, 1mA≤I_{OUT}≤200mA	—	0.1	—	mV/mA
$\Delta V_{OUT}/\Delta V_{IN}$	Line regulation	I_{OUT}=1mA, V_{OUTNOM}+2V≤V_{IN}≤7V	—	0.1	—	mV/V
I_{LIMIT}	Current limit	V_{IN}=V_{OUTNOM}+2V	—	800	—	mA
ISHORT	Short current		—	120	—	mA
TSHDN	Thermal shutdown temperature	Shutdown, temperature increasing	—	150	—	°C
		Reset, temperature decreasing	—	120	—	
V_{ENH}	EN high level	Enabled	1.07	—	1.2	V
V_{ENL}	EN low level	Shutdown	—	—	0.99	V

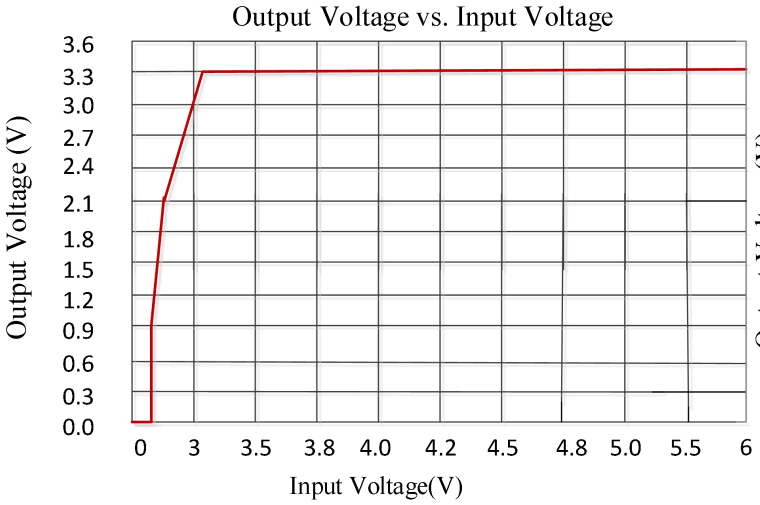
Note：

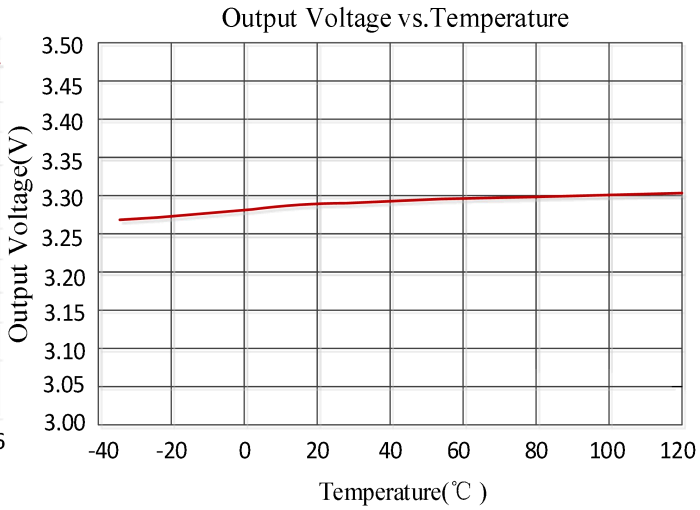
(1) Dropout Voltage is the voltage difference between the input and the output at which the output voltage drops 2% below its nominal value.

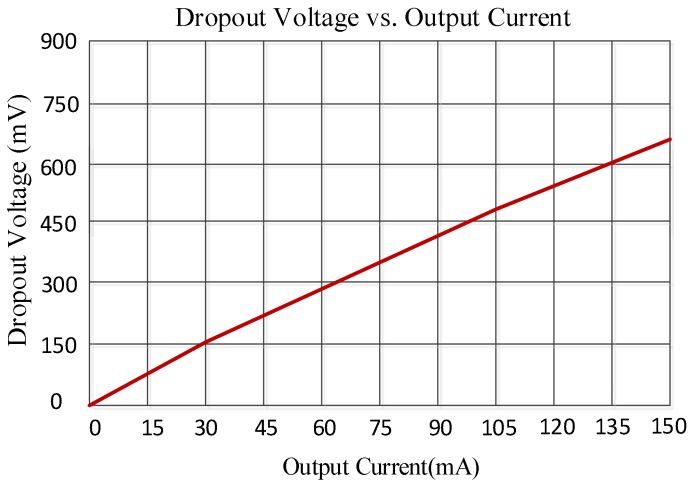
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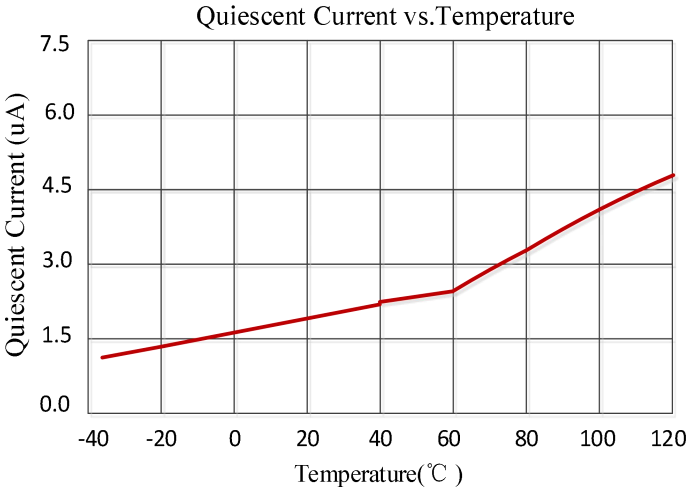
Typical Characteristics

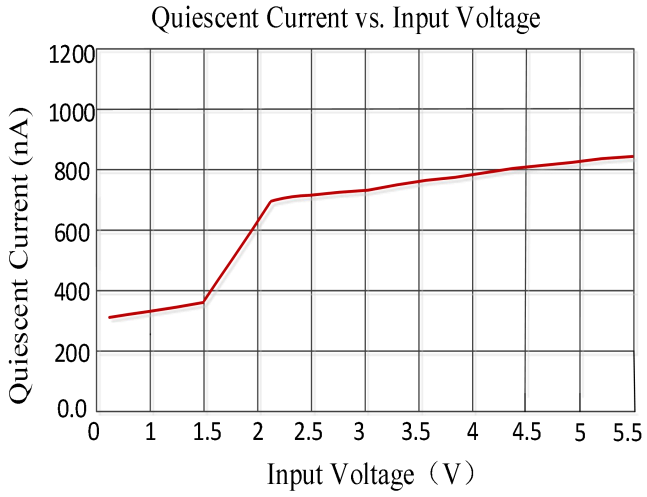
(Test condition: T_A=25°C,C_{IN}=1uF ， V_{IN}=V_{OUTNOM}+1V ， C_{OUT}=1uF unless otherwise note)

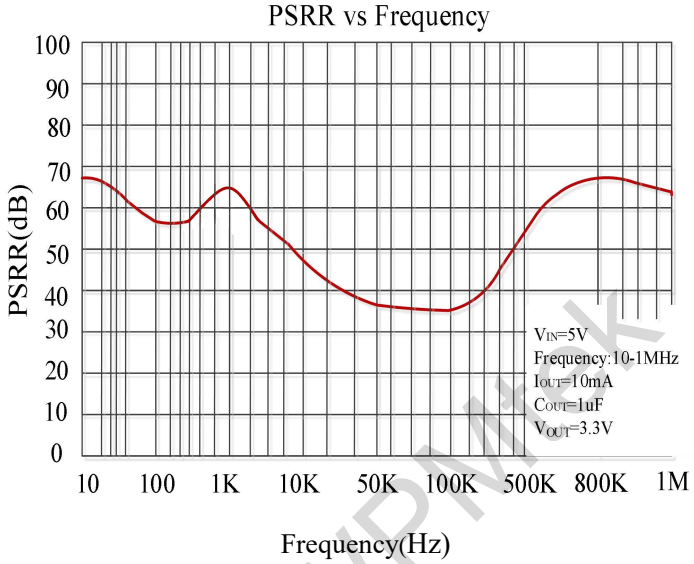






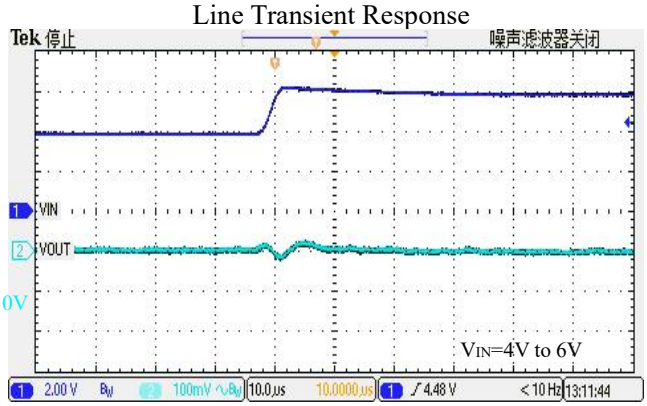






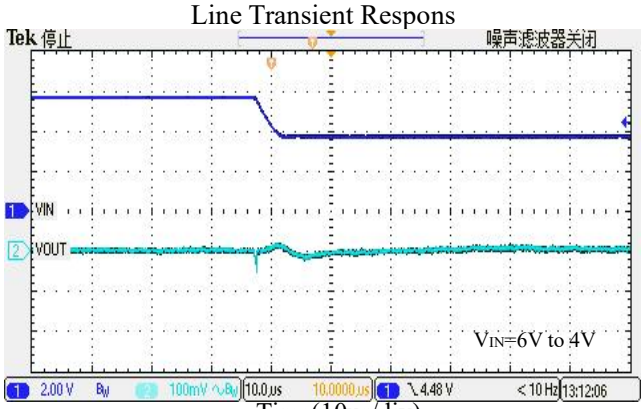
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Line Transient Response



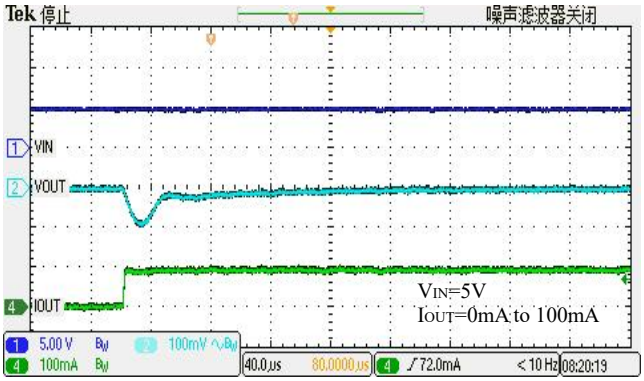
Time(10us/div)

Line Transient Respons



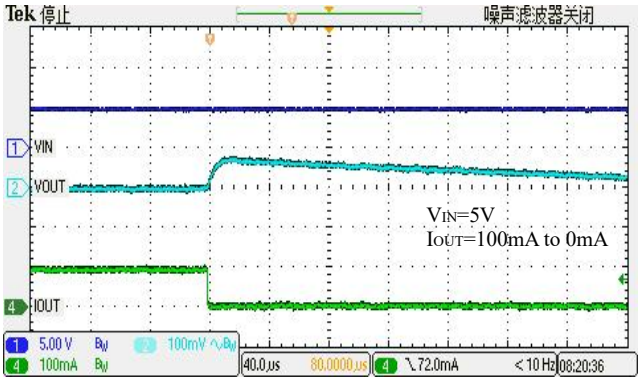
Time(10us/div)

Load Transient Response



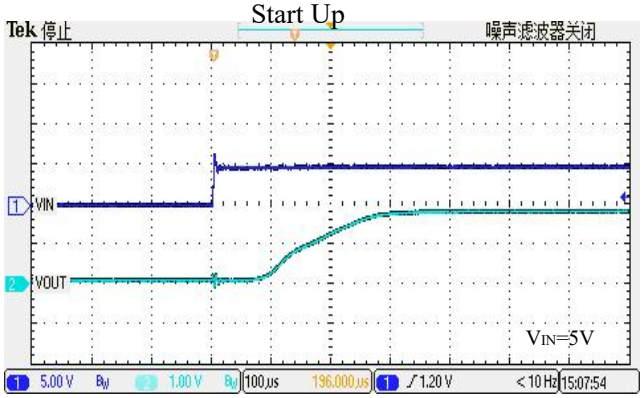
Time(40us /div)

Load Transient Response



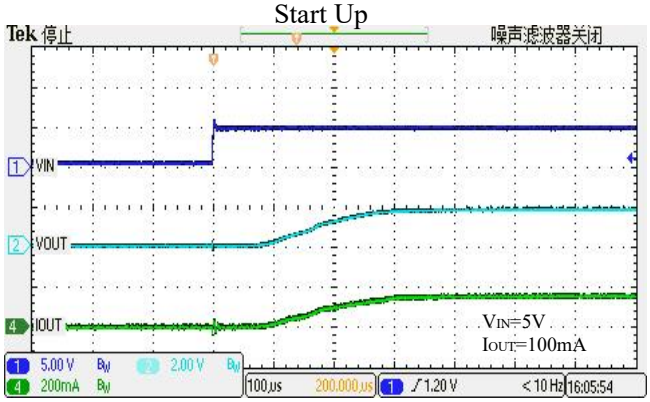
Time(40us/div)

Start Up



Time(100us/div)

Start Up



Time(100us/div)

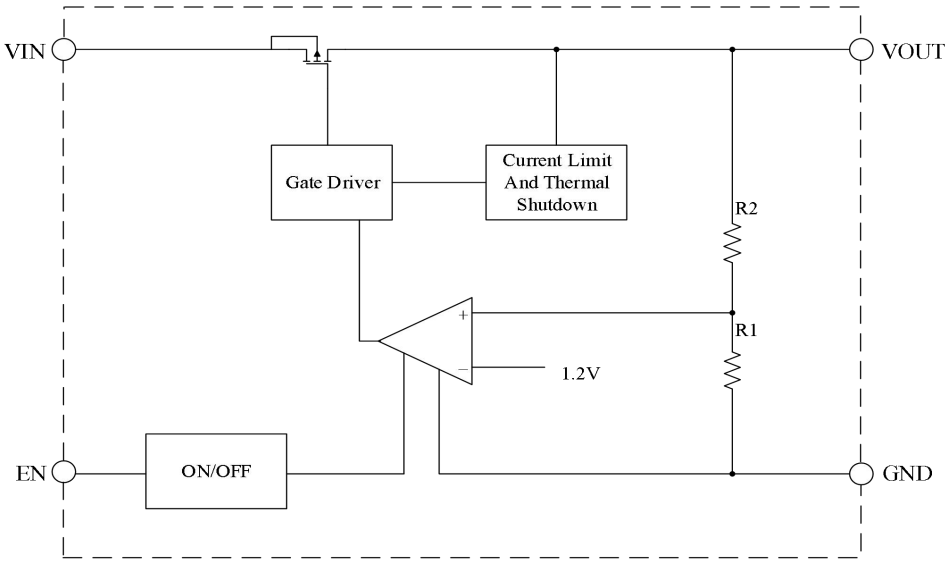
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Detailed Description

Overview

The WL9002H series is an ultra-small, low dropout (LDO) linear regulator that can source 500mA of output current.The WL9002H has fast response to input voltage transient and load current transient, and ensures no overshoot voltage during WL9002H startup and short circuit recovery.The WL9002H series has thermal shutdown, current limit, and short circuit protections for added safety. The WL9002H series contains eight fixed output voltages of 1.2V, 1.2V, 1.8V, 2.5V, 2.8V, 3.0V, 3.3V, 3.6V.

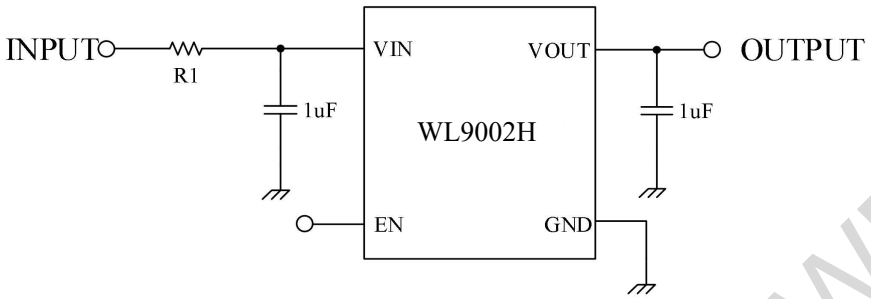
Functional Block Diagram



Functional Block Diagram

Input Capacitor and Output Capacitor

A 1uF ceramic capacitor is recommended to connect between VIN and GND pins to decouple input power supply glitch and noise. The amount of the capacitance may be increased without limit. This input capacitor must be located as close as possible to the device to assure input stability and less noise. For PCB layout, a wide copper trace is required for both VIN and GND.



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For the stability of LDO, an output capacitor is required. The recommended minimum output capacitance is 1 uF. It is recommended to use Ceramic capacitor with a temperature characteristic of X5R or X7R. A higher capacitance value helps to improve the transient response of the load/line. The output capacitance can be increased to maintain a lower down/overshoot. Place the output capacitor as close as possible to the VOUT and GND pins.

Current Limit and Short Circuit Protection

When output current at VOUT pin is higher than current limit threshold or the VOUT pin is direct short to GND, the current limit protection will be triggered and clamp the output current at a pre-designed level to prevent over-current and thermal damage.

Power Dissipation and Thermal Protection

The WL9002H has internal thermal sense and protection circuits. When excessive power dissipation happens on the device, such as short circuit at the output pin or very heavy load current with a large voltage drop across the device, the internal thermal protection circuit will be triggered, and it will shut down the power MOSFET to prevent the LDO from damage. As soon as excessive thermal condition is removed and the temperature of the device drops down, the thermal protection circuit will lease the control of the power MOSFET, and the LDO device goes to normal operation.

Power dissipation caused by voltage drop across the LDO and by the output current flowing through the device needs to be dissipated out from the chip. The maximum junction temperature is dependent on power dissipation,package, the PCB layout, number of used Cu layers, Cu layers thickness and the ambient temperature.

During normal operation, LDO junction temperature should not exceed 150°C, or else it may result in deterioration of the properties of the chip. Using below equations to calculate the power dissipation and estimate the junction temperature.

The power dissipation can be calculated using Equation 1.

$$P_D = (V_{IN} - V_{OUT}) \times I_{OUT} \quad (1)$$

The junction temperature can be estimated using Equation . $R_{\theta JA_EVM}$ is the junction-to-ambient thermal resistance based on customer's PCB. Verify the application and allow sufficient margins in the thermal design by the Equation 2.

$$T_J = T_A + P_D \times R_{\theta JA_EVM} \quad (2)$$

$R_{\theta JA_EVM}$ is a critical parameter and depends on many factors such as the following:

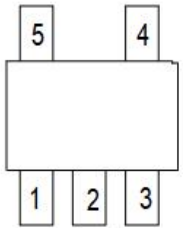
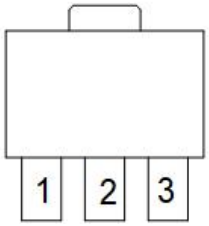
· Power dissipation
· Air temperature/flow
· PCB area
· Copper heat-sink area
· Number of thermal vias under the package
· Adjacent component placement

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Ordering Information

WL-9002H-XX-XX

WPMtek	Product name	Package	Output voltage
		S5:SOT23-5 P3:SOT89-3 D4:DFN1×1	12:1.2V 15:1.5V 18:1.8V 25:2.5V 30:3.0V 33:3.3V 36:3.6V

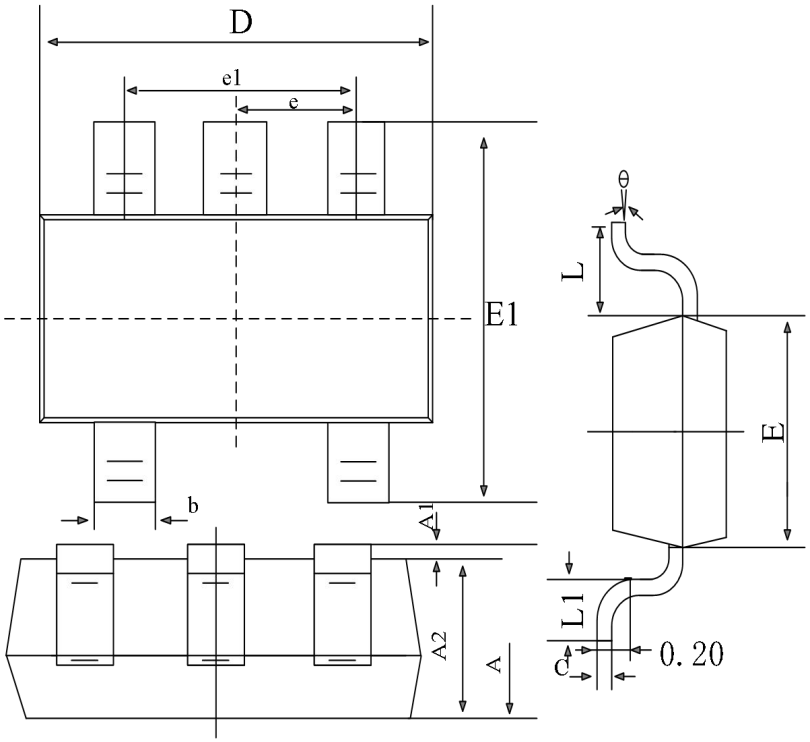
Package outline			
Minimal packaging	SOT23-5 3000pcs/Reel	SOT89-3 1000pcs/Reel	DFN1×1 10000pcs/Reel
Marking	9002HXX Output voltage	9002HXX Output voltage	Output voltage XX XX Internal code variable

WL9002HS5-12	WL9002HS5-15	WL9002HS5-18	WL9002HS5-25	WL9002HS5-30
9002H12	9002H15	9002H18	9002H25	9002H30
WL9002HS5-33	WL9002HS5-36	WL9002HP3-12	WL9002HP3-15	WL9002HP3-18
9002H33	9002H36	9002H12	9002H15	9002H18
WL9002HP3-25	WL9002HP3-30	WL9002HP3-33	WL9002HP3-36	WL9002HD4-12
9002H25	9002H30	9002H33	9002H36	12 XX
WL9002HD4-15	WL9002HD4-18	WL9002HD4-25	WL9002HD4-30	WL9002HD4-33
15 XX	18 XX	25 XX	30 XX	33 XX
WL9002HD4-36				
36 XX				

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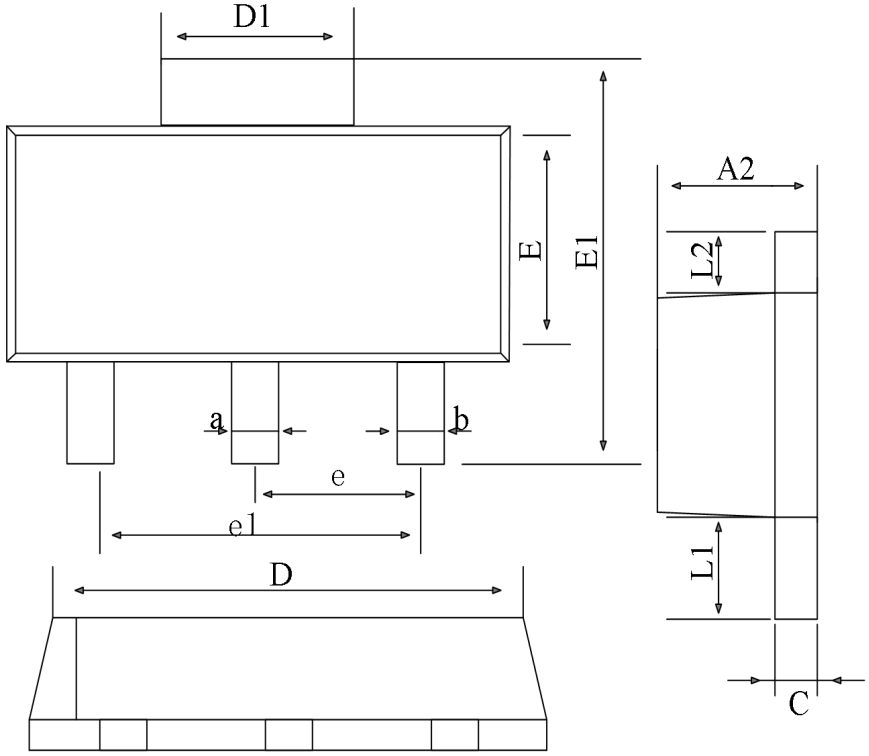
Package Outline

SOT23-5



REF.	Millimeter	
	Min.	Max.
A	1.05	1.25
A1	0	0.1
A2	1.05	1.15
b	0.3	0.5
c	0.1	0.2
D	2.85	3.05
E	1.5	1.7
E1	2.65	2.95
e	0.95(BSC)	
e1	1.8	2.0
L	0.3	0.6
θ	0°	8°

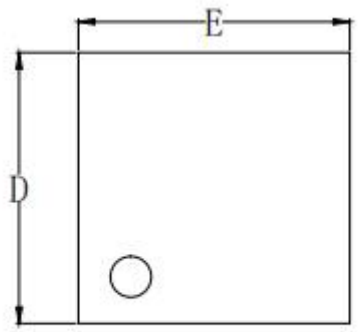
SOT89-3



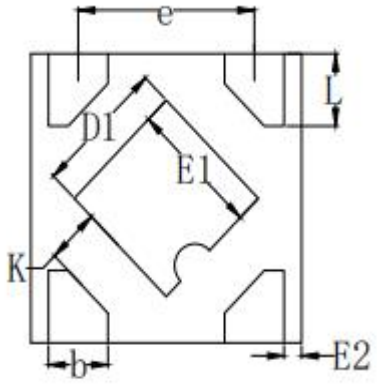
REF.	Millimeter	
	Min.	Max.
A2	1.4	1.6
a	0.45	0.55
b	0.38	0.48
c	0.36	0.46
D	4.40	4.60
D1	1.60	1.80
E	2.40	2.60
E1	4.00	4.30
e	1.00	2.00
e1	2.95	3.05
L1	0.80	1.00
L2	0.65	0.75

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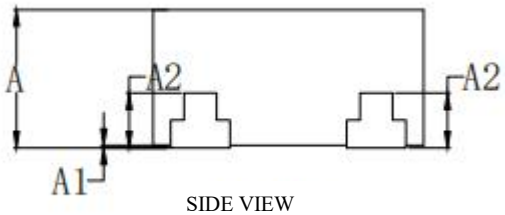
DFN1×1



TOP VIEW



BOTTOM VIEW

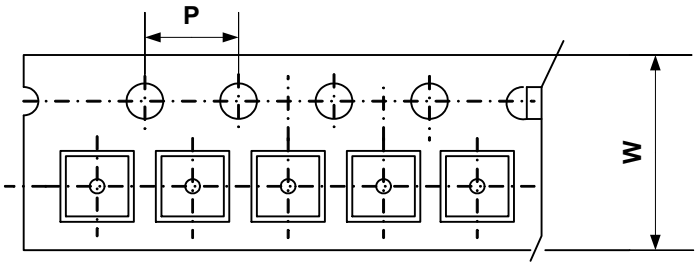


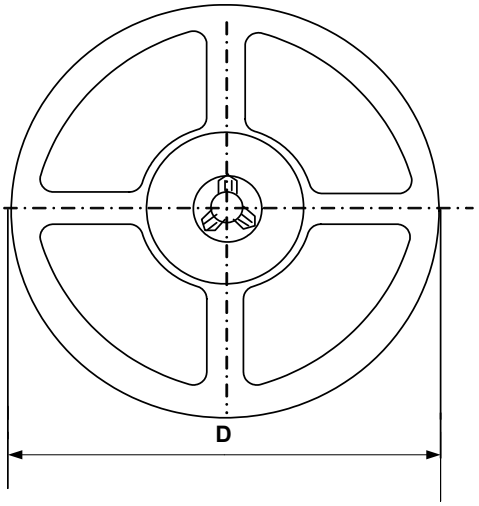
SIDE VIEW

REF.	Millimeter		
	Min.	Nom.	Max.
A	0.45	0.50	0.55
	0.50	0.55	0.60
A1	0.00	--	0.05
A2	0.203 TIY		
b	0.17	0.22	0.27
D	0.95	1.00	1.05
D1	0.43	0.48	0.53
E	0.95	1.00	1.05
E1	0.43	0.48	0.53
E2	0.065 TIY		
e	0.65 BSC		
k	0.20 BSC		
L	0.20	0.25	0.30

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Packing Information





Type	W(mm)	P(mm)	D(mm)	Qty (pcs)
SOT23-5	8.0±0.1 mm	4.0±0.1 mm	180±1 mm	3000pcs
SOT89-3	12.0±0.1 mm	4.0±0.1 mm	180±1 mm	1000pcs
DFN1×1	8.0±0.1 mm	4.0±0.1 mm	180±1 mm	10000pcs

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