SOT-143 Plastic-Encapsulate ESD Protection Diodes

Features

- Low leakage current
- Low clamping voltage
- IEC 61000-4-2 (ESD Air): ±20kV
- IEC 61000-4-2 (ESD Contact): ±20kV
- IEC 61000-4-5 (Lightning 8/20μs):5A

Reverse Working Voltage
5V Max.
Low capacitance
0.8 pF(Max.)

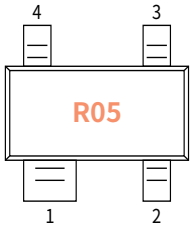
Applications

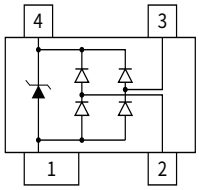
- Cellular Handsets and Accessories
- Personal Digital Assistants
- Notebooks and Handhelds
- Portable Instrumentation,Digital Cameras
- Peripherals, Audio Players, Industrial Equipment

SOT-143



Function Diagram



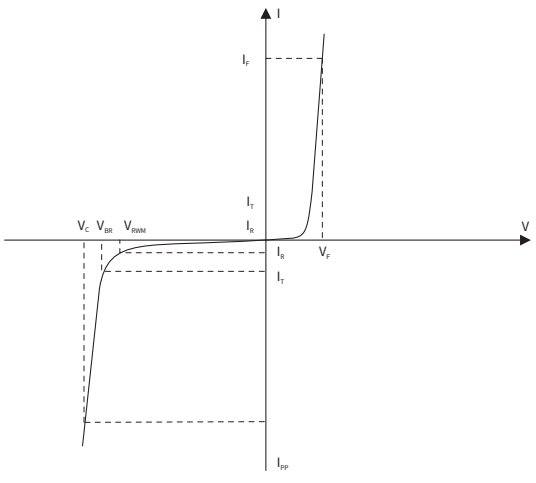


Maximum Ratings (Ta=25°C Unless otherwise specified)

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
V_{ESD}	Electrostatic Discharge Voltage	ESD per IEC 61000-4-2(Air)	±20	KV
		ESD per IEC 61000-4-2(Contact)	±20	KV
P_{PP}	Peak Pulse Power	tp = 8/20 μs	125	W
I_{PP}	Rated Peak Pulse Current	tp = 8/20 μs	5	A
T_J	Operating JunctionTemperature Range	—	-55 to +150	°C
T_{STG}	Storage Temperature Range	—	-55 to +150	°C

Electrical Parameter

SYMBOL	PARAMETER
V_C	Clamping Voltage @ I_{PP}
V_{BR}	Breakdown Voltage @ I_T
I_{PP}	Peak Pulse Current
I_T	Test Current
I_R	Reverse Leakage Current @ VRWM
V_{RWM}	Peak Reverse Working Voltage
P_{PP}	Peak Pulse Power Dissipation
C_J	Junction Capacitance @ V_R=0V,f=1MHz
I_F	Forward Current
V_F	Forward Voltage @I_F



Electrical Characteristics (Ta=25°C Unless otherwise specified)

PARAMETER	SYMBOL	CONDITION	Min	Typ	Max	UNIT
Peak Reverse Working Voltage	V_{RWM}	T_a=25°C	—	—	5.0	V
Breakdown Voltage	V_{BR}	I_T=1mA,T_a=25°C	6.0	—	—	V
Reverse Leakage Current	I_R	V_{RWM}=5.0V,T_a=25°C	—	—	0.5	μA
Clamping Voltage	V_C	I_{PP}=1A,t_p=8/20μs	—	—	9.8	V
Clamping Voltage	V_C	I_{PP}=5A,t_p=8/20μs	—	—	25	V
Junction Capacitance	C_J	V_{RMW}=0V,f=1MHz	—	0.5	0.8	pF

Ratings And Characteristics Curves (Ta=25°C Unless otherwise specified)

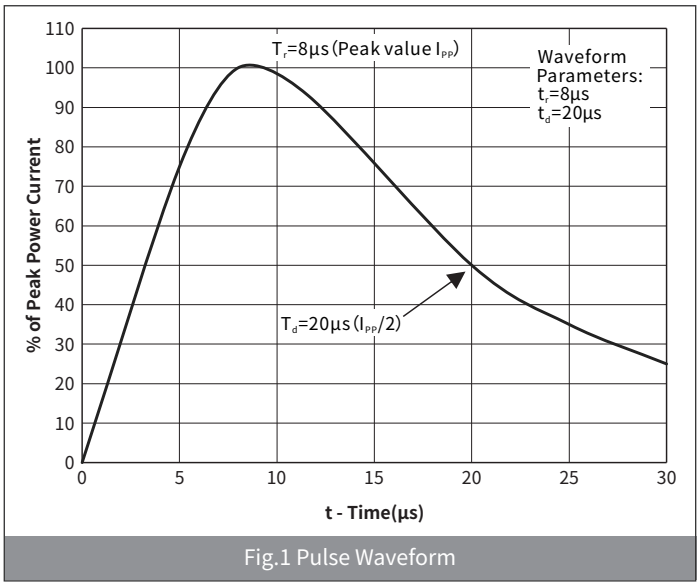


Fig.1 Pulse Waveform

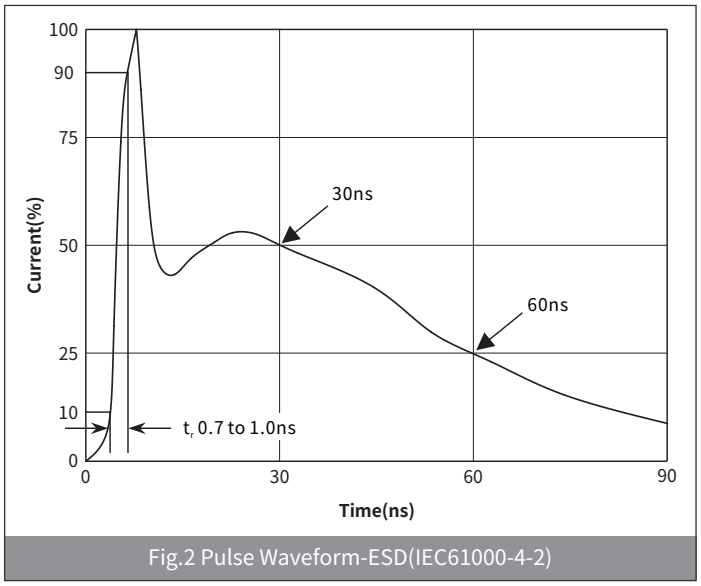


Fig.2 Pulse Waveform-ESD(IEC61000-4-2)

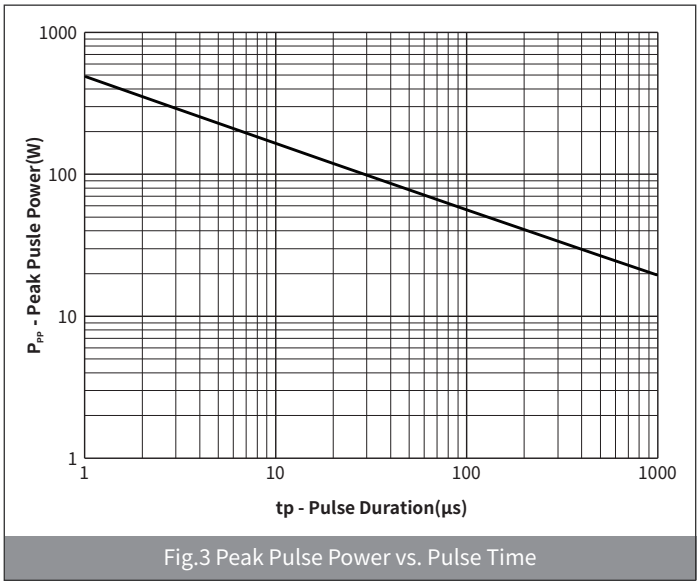


Fig.3 Peak Pulse Power vs. Pulse Time

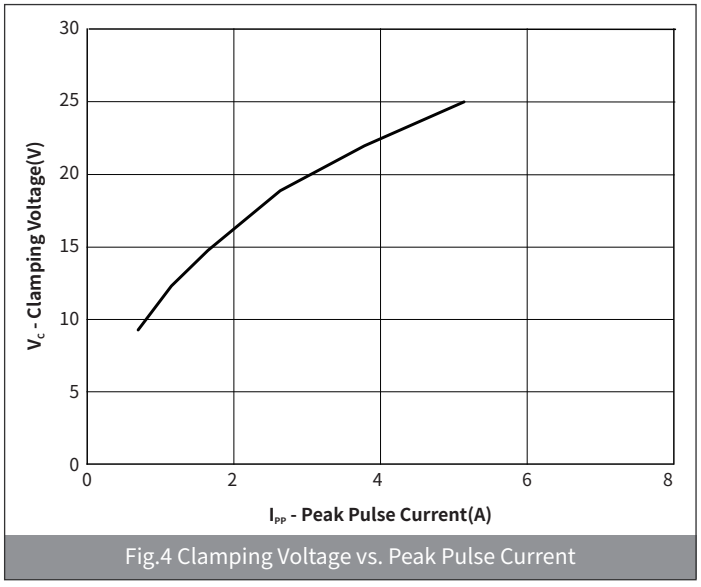
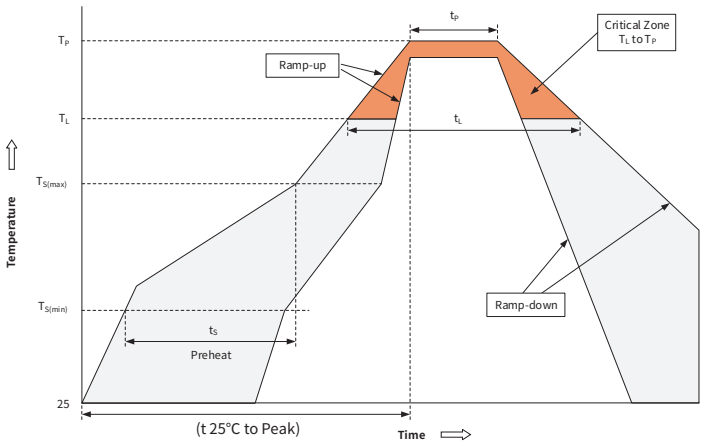


Fig.4 Clamping Voltage vs. Peak Pulse Current

Ordering Information

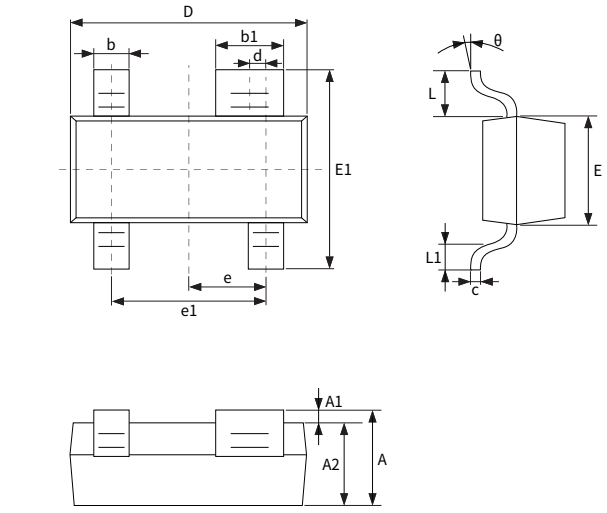
PREFERED P/N	PACKAGE	SIZE(mm)	DELIVERY MODE	MPQ(PCS)
H5VLT1U	SOT-143	2.90×1.30×1.00	7"	3000

Recommended Soldering Conditions



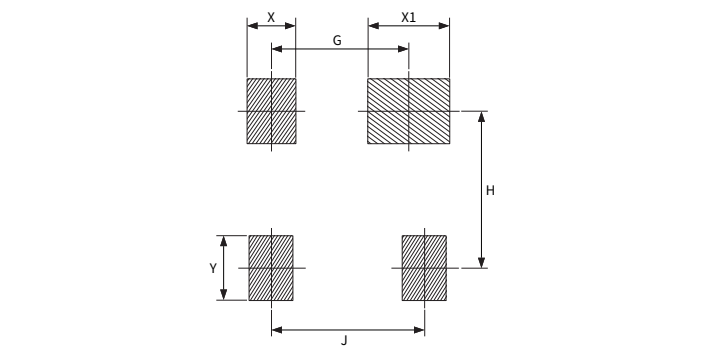
Profile Feature		Pb-Free Assembly
Pre-heat	Temperature Min ($T_{S(min)}$)	+150°C
	Temperature Max($T_{S(max)}$)	+200°C
	Time (Min to Max) (t_S)	60-180 secs.
Average ramp up rate (Liquid us Temp (T_L) to peak)		3°C /sec. Max
$T_{S(max)}$ to T_L - Ramp-up Rate		3°C /sec. Max
Reflow	Temperature(T_L)(Liquid us)	+217°C
	Temperature(t_L)	60-150 secs.
Peak Temp (T_P)		+260(+0/-5)°C
Time within 5°C of actual Peak Temp (t_P)		20-40secs
Ramp-down Rate		6°C /sec. Max
Time 25°C to Peak Temp (T_P)		8 min. Max
Do not exceed		+260°C

Package Outline Dimensions (SOT-143)



Symbol	Dimensions			
	Millimeters		Inches	
	Min.	Max.	Min.	Max.
A	0.90	1.15	0.035	0.045
A1	-	0.10	-	0.004
A2	0.90	1.05	0.035	0.041
b	0.30	0.50	0.012	0.020
b1	0.75	0.90	0.030	0.035
c	0.08	0.15	0.003	0.006
D	2.80	3.00	0.110	0.118
d	0.200TYP		0.008TYP	
E	1.20	1.40	0.047	0.055
E1	2.25	2.55	0.089	0.100
e	0.950TYP		0.037TYP	
e1	1.80	2.00	0.071	0.079
L	0.550REF		0.022REF	
L1	0.30	0.50	0.012	0.020
θ	-	8°	-	8°

Suggested Pad Layout



Symbol	Dimensions			
	Millimeters		Inches	
	Min.	Max.	Min.	Max.
G	-	1.70	-	0.066
H	-	1.94	-	0.076
J	-	1.90	-	0.074
X	0.60	-	0.023	-
X1	1.00	-	0.039	-
Y	0.80	-	0.031	-