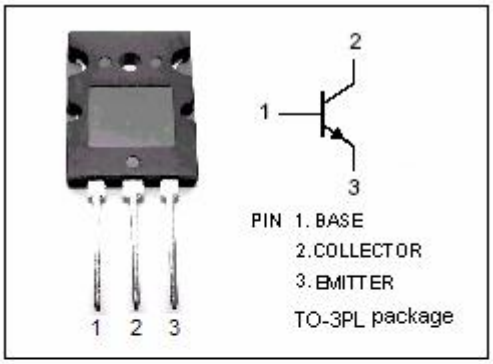
SPTECH Silicon NPN Power Transistor



DESCRIPTION

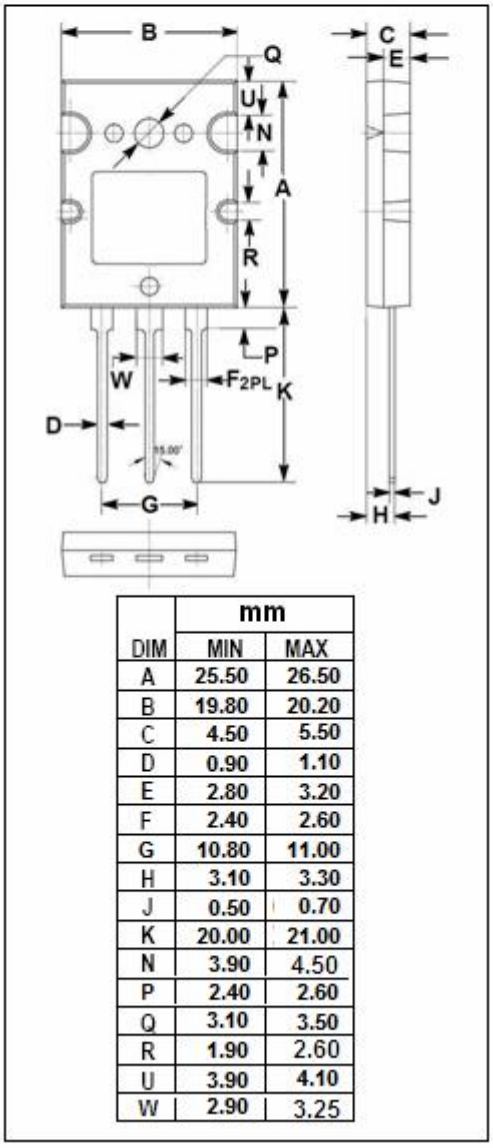
- High breakdown voltage, and high reliability
- Wide area of safe operation (ASO)
- High-speed switching

APPLICATIONS

- Designed for horizontal deflection output applications.

ABSOLUTE MAXIMUM RATINGS(T_a=25℃)

SYMBOL	PARAMETER	VALUE	UNIT
V_{CBO}	Collector-Base Voltage	1500	V
V_{CEO}	Collector-Emitter Voltage	800	V
V_{EBO}	Emitter-Base Voltage	6	V
I_C	Collector Current-Continuous	20	A
I_{CM}	Collector Current-Pulse	30	A
P_C	Collector Power Dissipation @ T_C=25℃	200	W
T_J	Junction Temperature	150	℃
T_{stg}	Storage Temperature Range	-55~150	℃



DIM	mm MIN	MAX
A	25.50	26.50
B	19.80	20.20
C	4.50	5.50
D	0.90	1.10
E	2.80	3.20
F	2.40	2.60
G	10.80	11.00
H	3.10	3.30
J	0.50	0.70
K	20.00	21.00
N	3.90	4.50
P	2.40	2.60
Q	3.10	3.50
R	1.90	2.60
U	3.90	4.10
W	2.90	3.25

SPTECH Silicon NPN Power Transistor

ELECTRICAL CHARACTERISTICS

T_C=25℃ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
$V_{CEO(SUS)}$	Collector-Emitter Sustaining Voltage	I_C= 10mA; I_B= 0	800			V
$V_{CE(sat)}$	Collector-Emitter Saturation Voltage	I_C=10A; I_B=2.8A			3.0	V
$V_{BE(sat)}$	Base-Emitter Saturation Voltage	I_C=10A; I_B=2.8A			1.5	V
I_{CBO}	Collector Cutoff Current	V_{CB}= 1500V; I_E= 0			1.0	mA
I_{EBO}	Emitter Cutoff Current	V_{EB}= 5V; I_C= 0			50	μA
h_{FE}	DC Current Gain	I_C= 10A; V_{CE}= 5V	5		12	
f_T	Current-Gain—Bandwidth Product	I_C= 0.1A ; V_{CE}= 10V		3		MHz
t_{stg}	Storage Time	I_C= 12A, I_{B1}=2.4A; I_{B2}= -4.8A		1.5	2.5	μs
t_f	Fall Time			0.12	0.2	μs